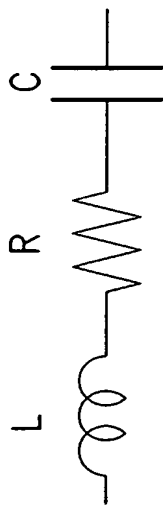
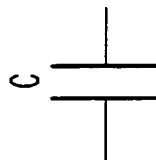


FIG. 1

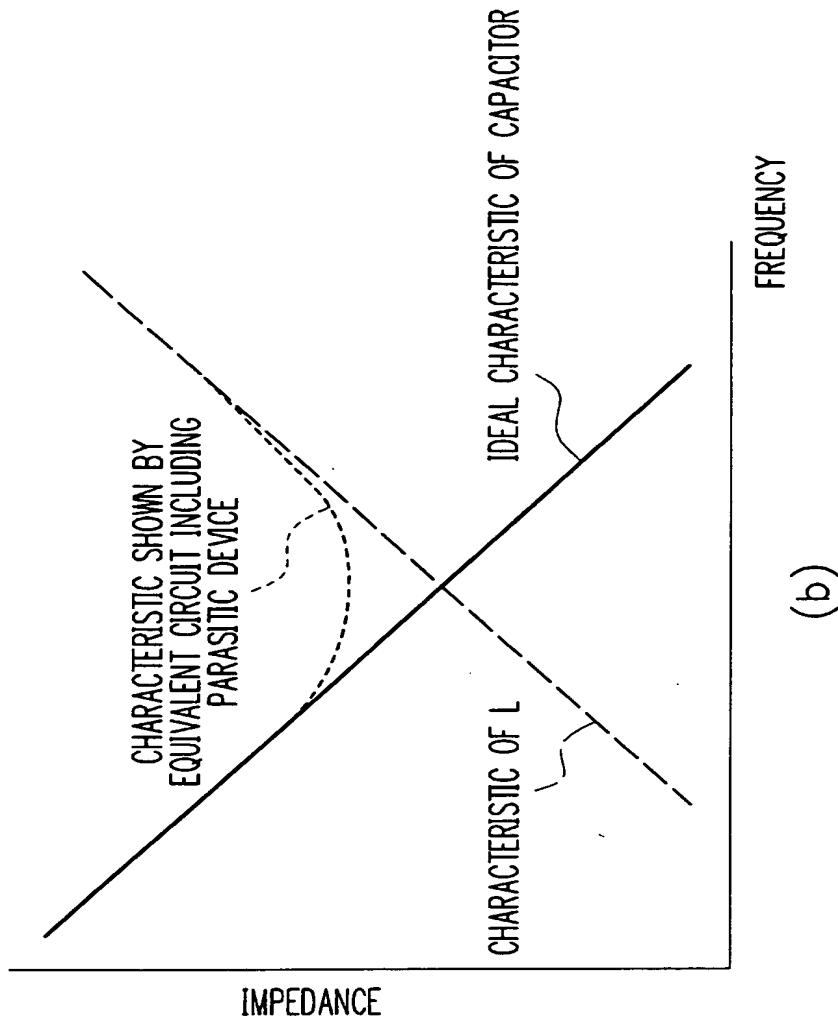


ACTUAL CAPACITOR
(EQUIVALENT CIRCUIT INCLUDING PARASITIC DEVICE)



IDEAL CHARACTERISTIC OF CAPACITOR

(a)



(b)

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Title: Broadband Circuit

Inventor: Hirokazu TOHYA

Atty. Dkt. No. 072982-0267

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F I G. 2

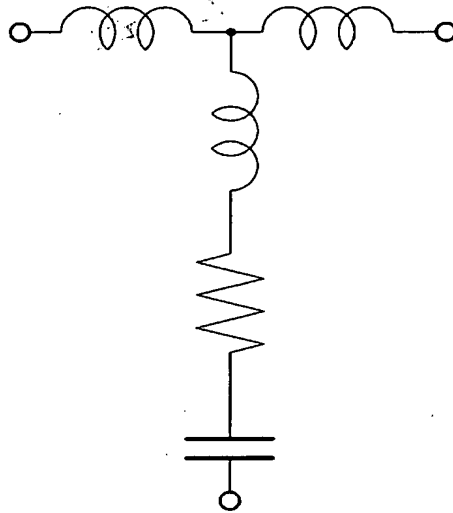


FIG. 3

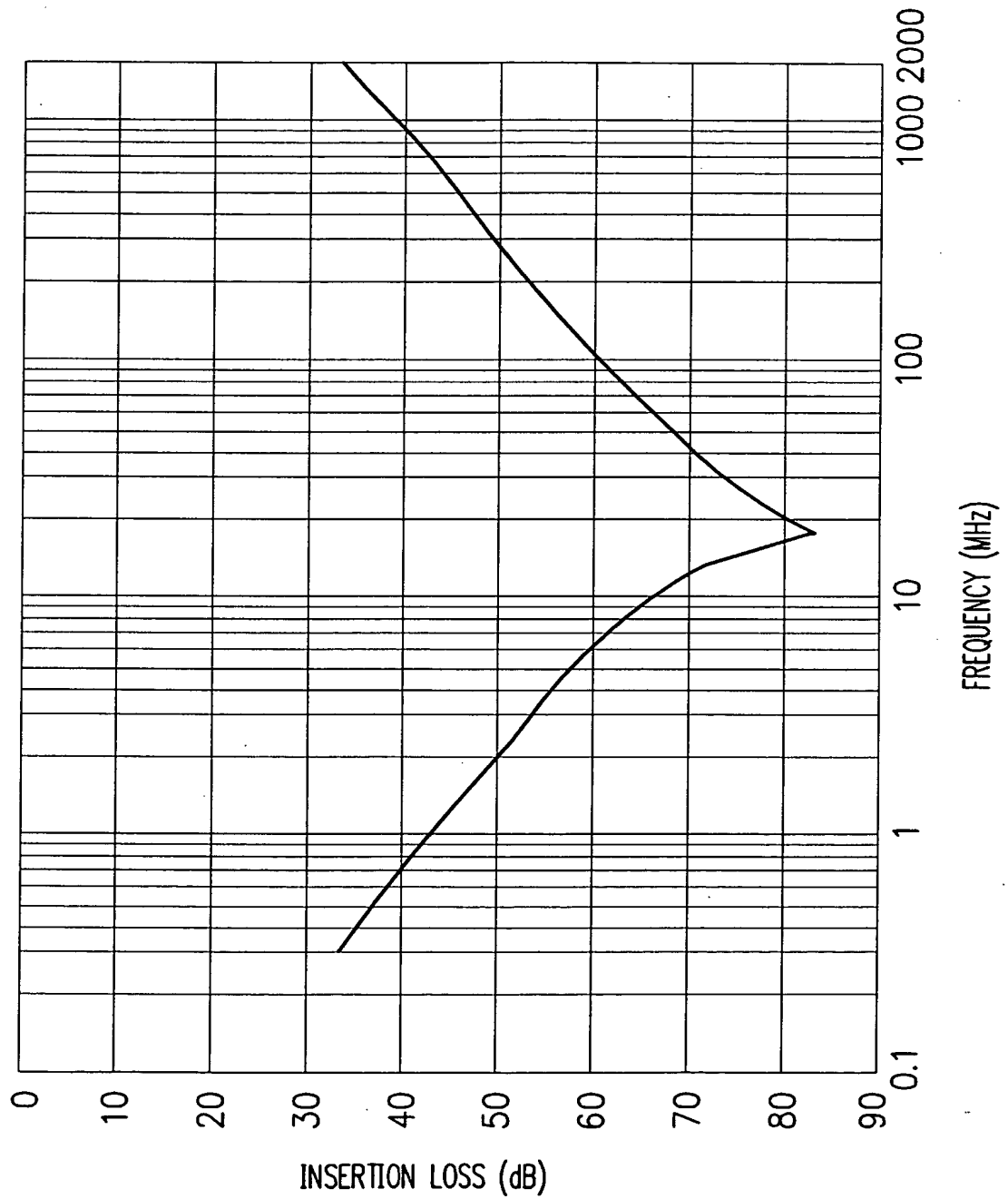
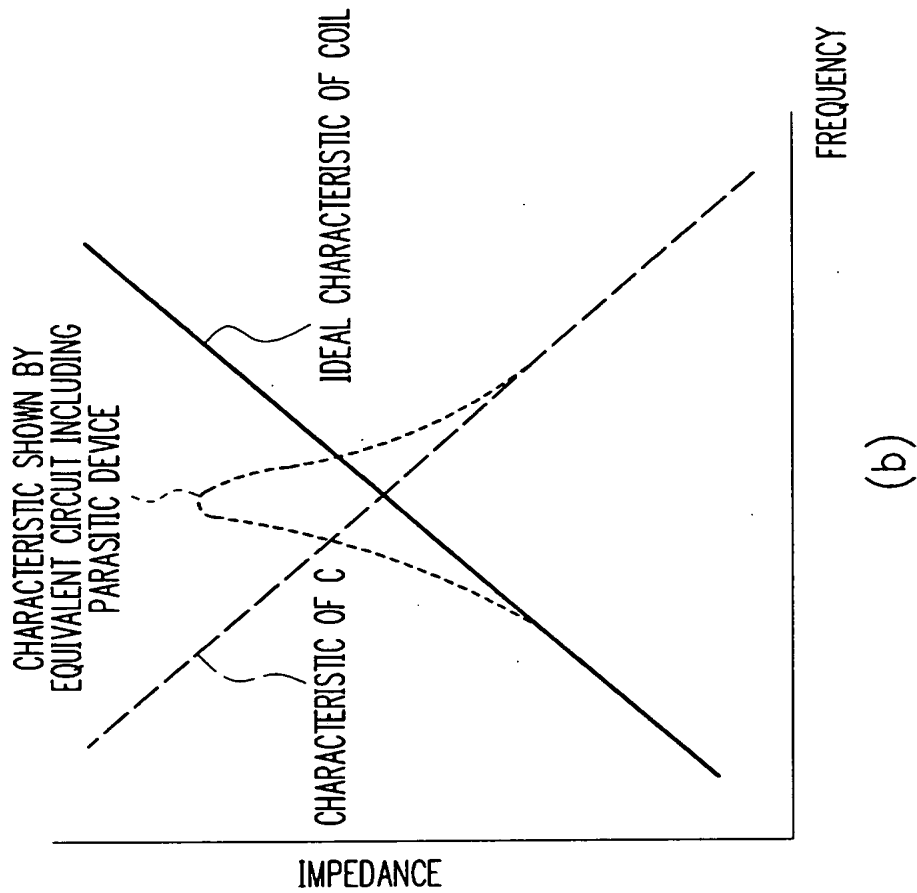
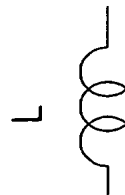
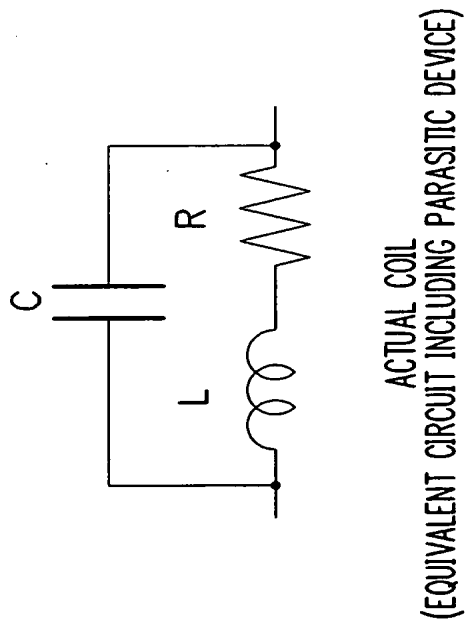


FIG. 4



(a)

(b)

FIG. 5

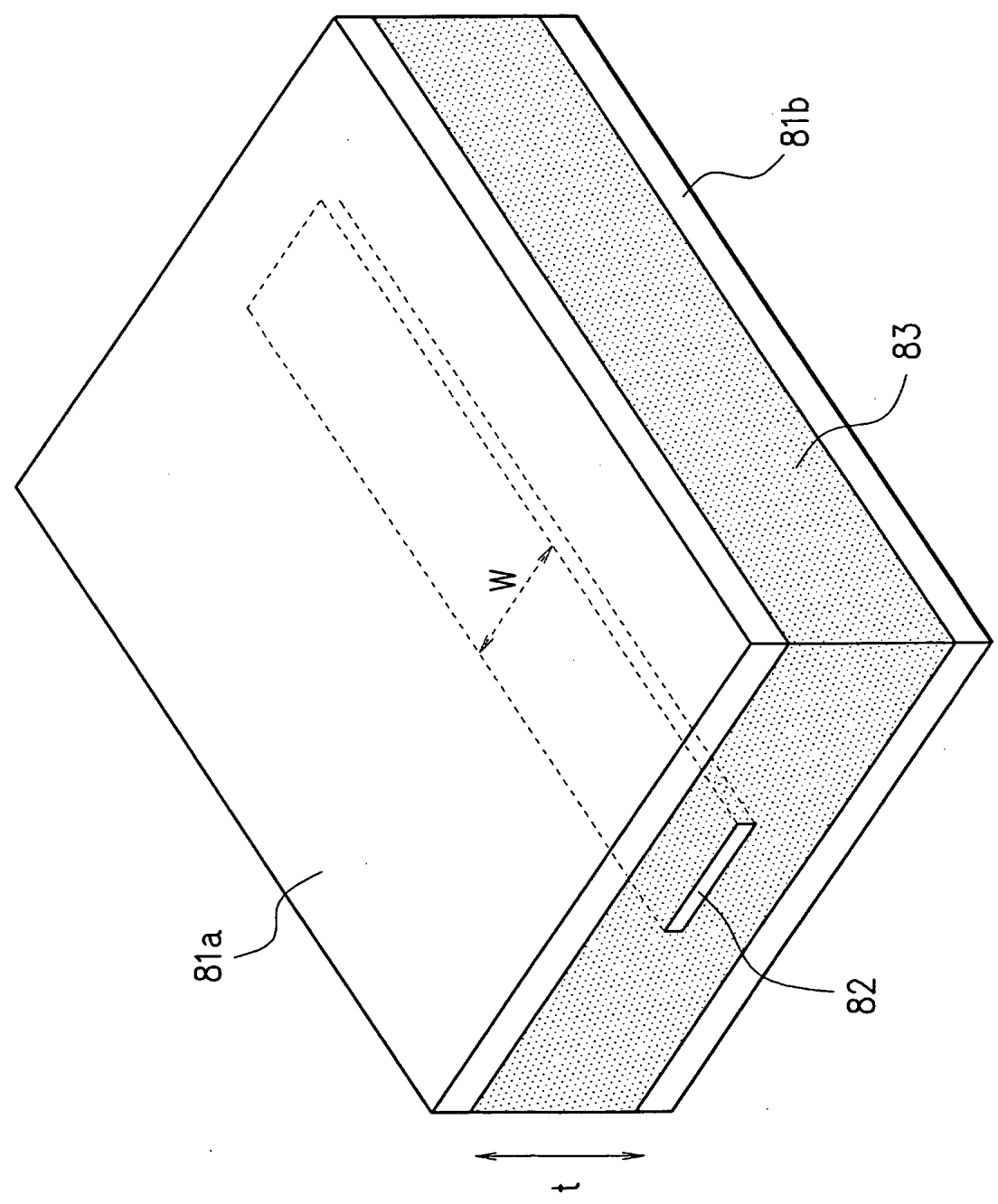


FIG. 6

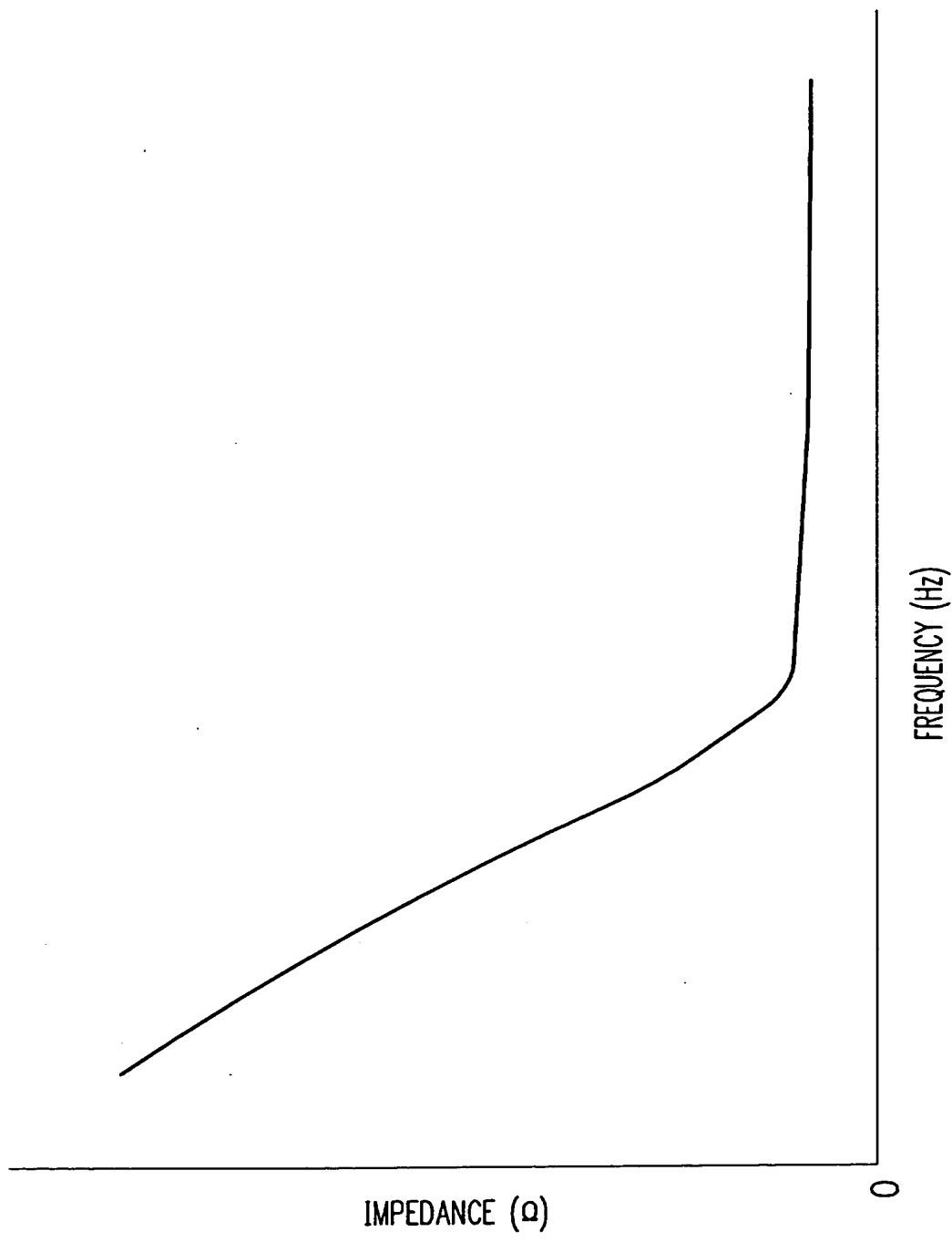
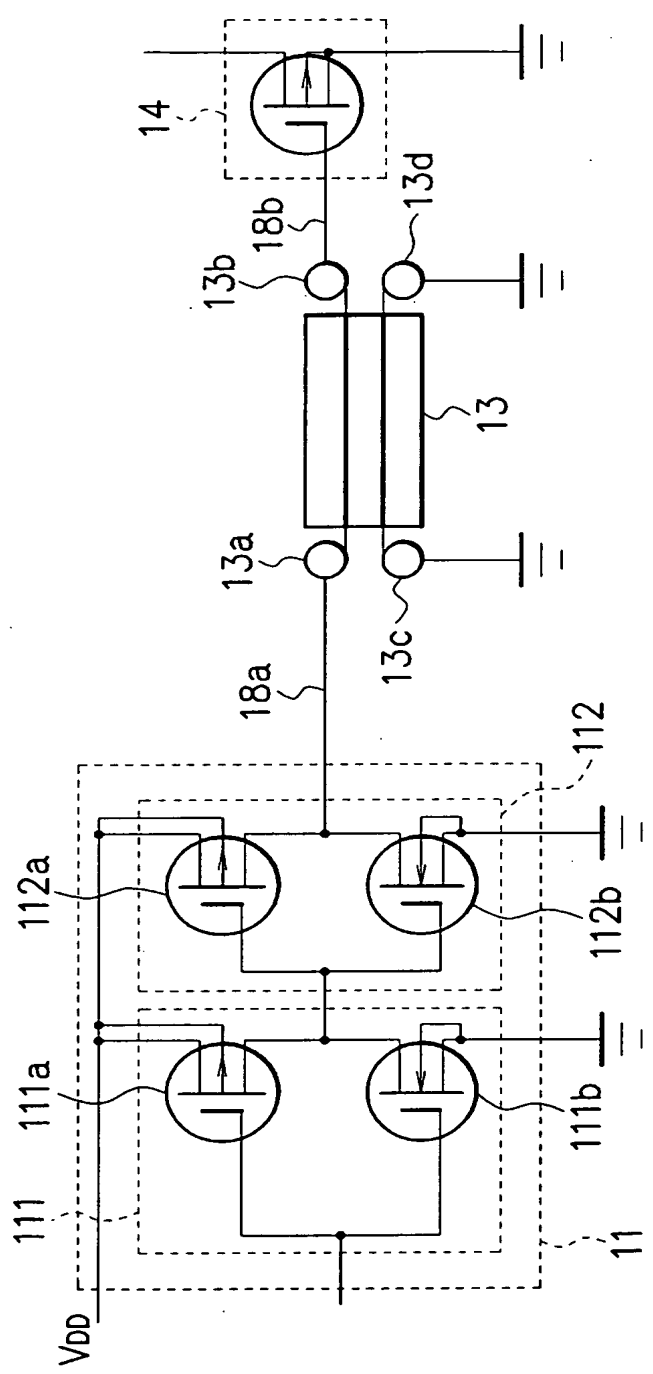


FIG. 7



F I G. 8

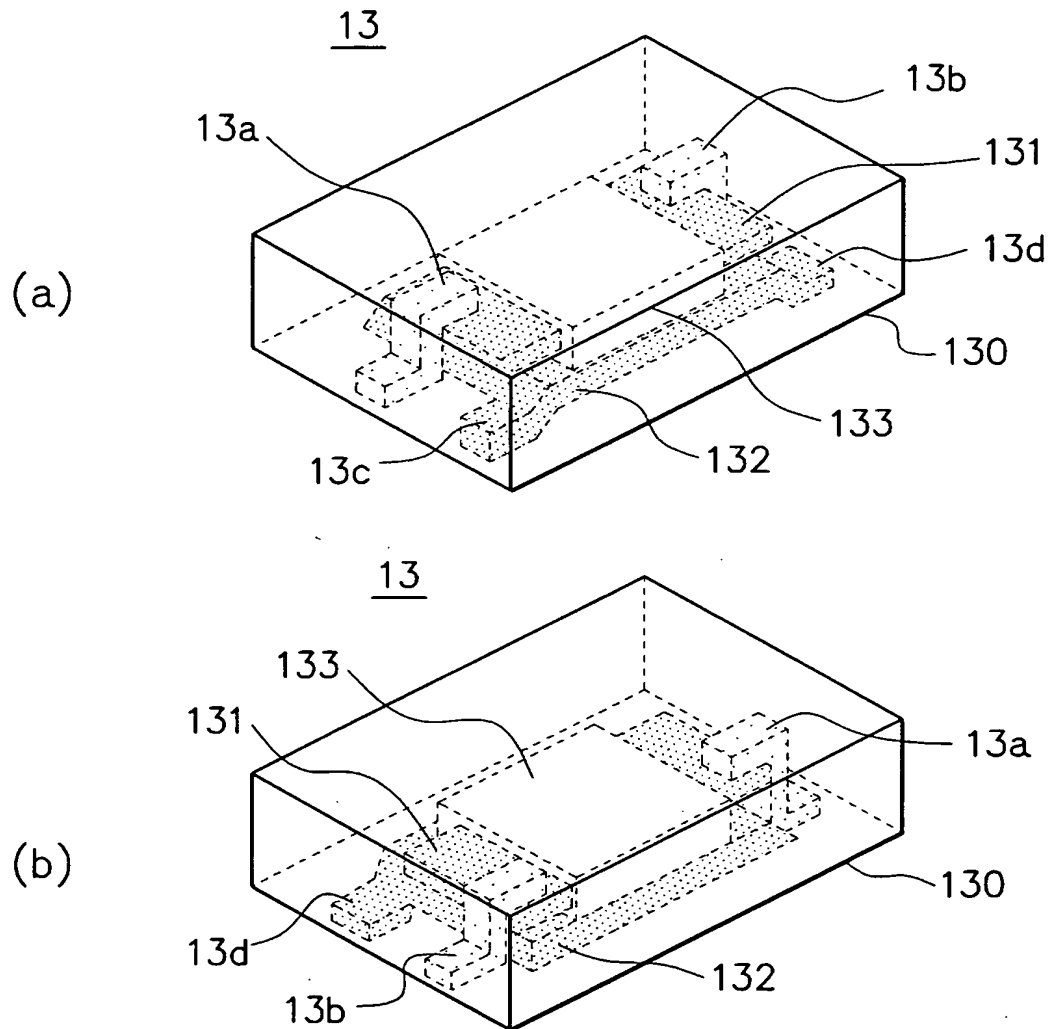


FIG. 9

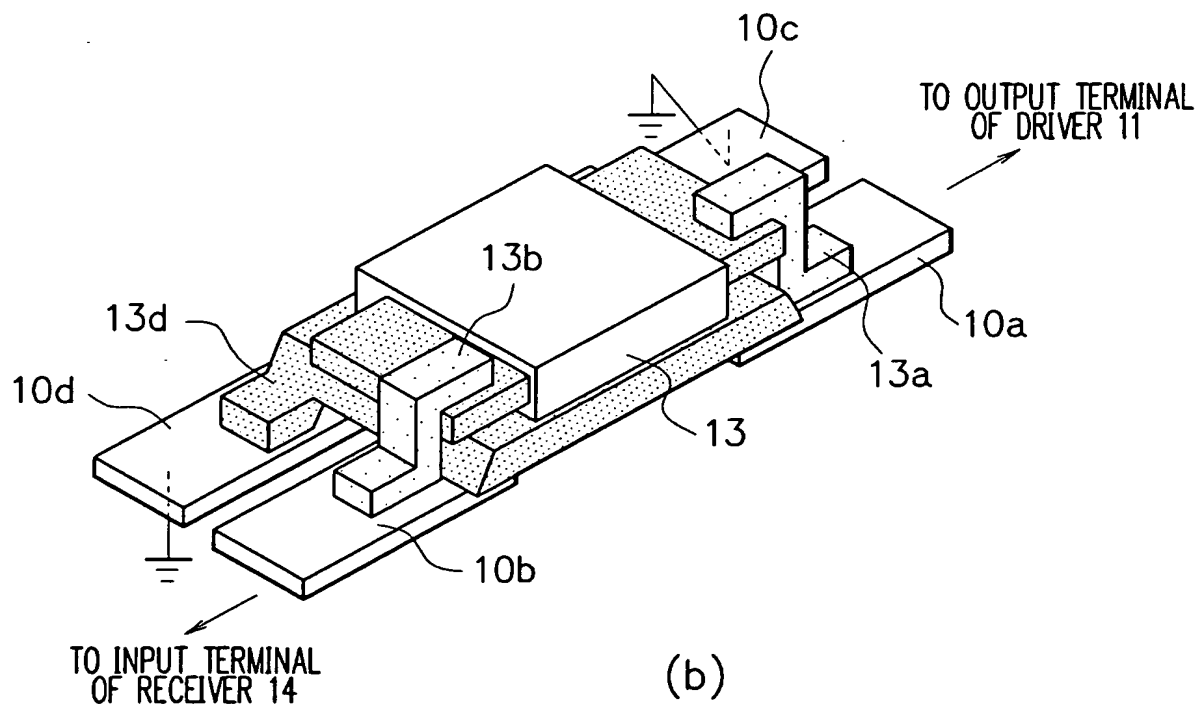
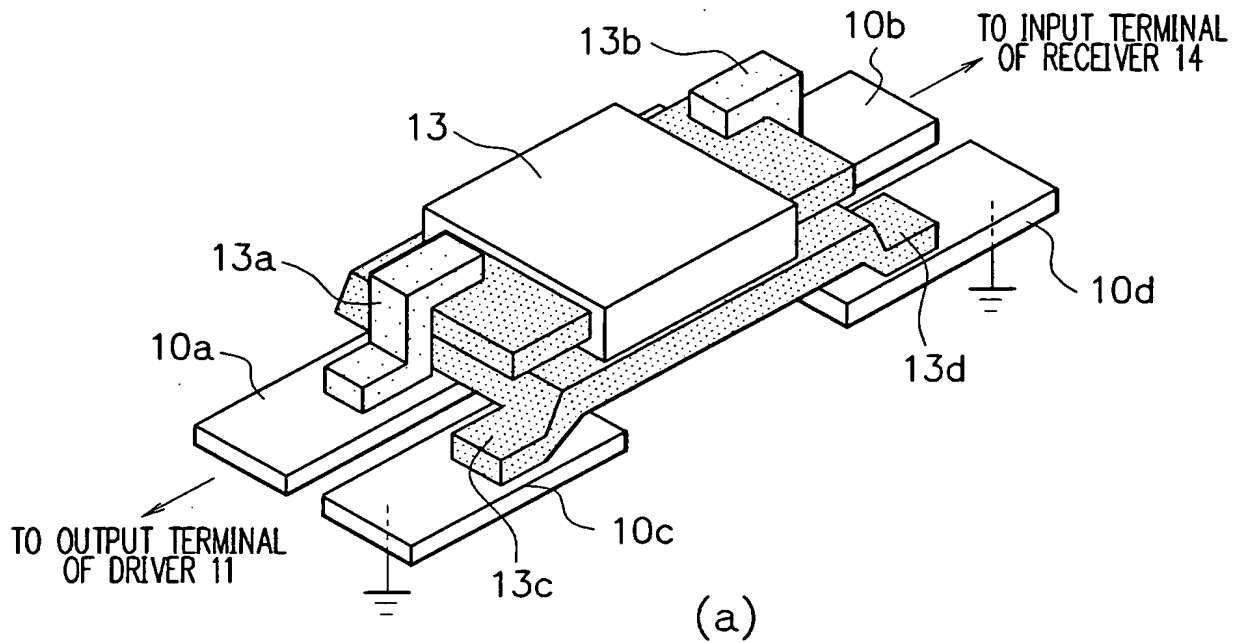


FIG. 10

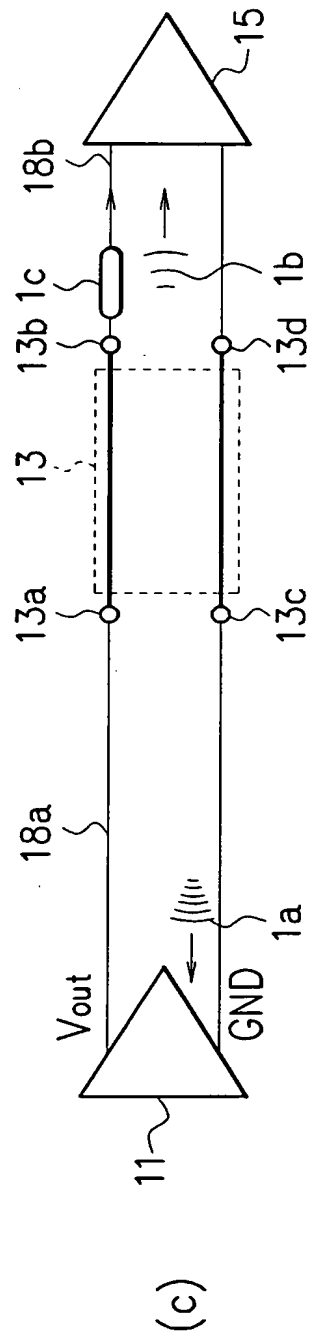
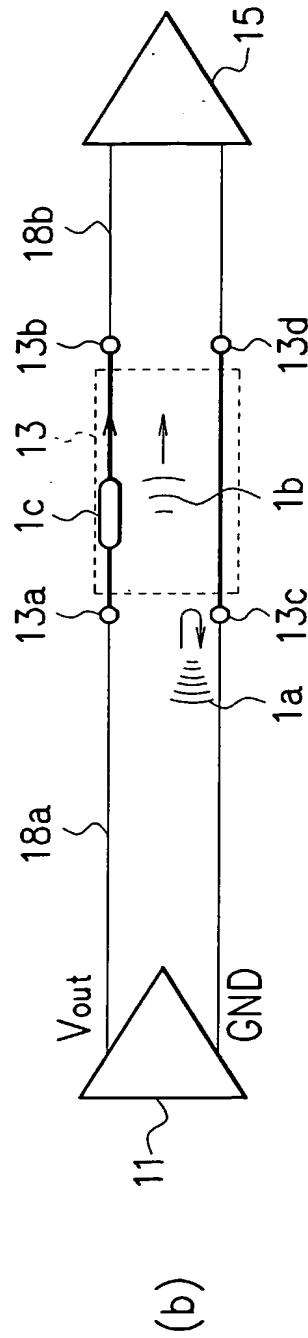
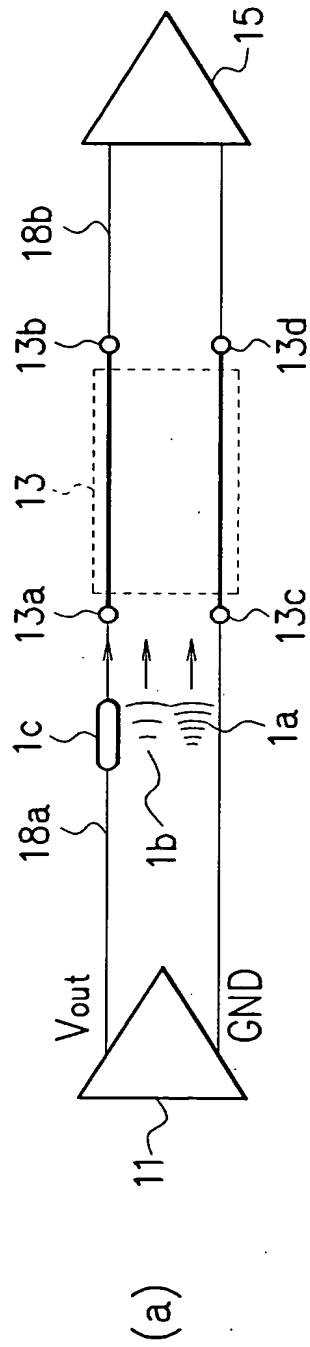


FIG. 11

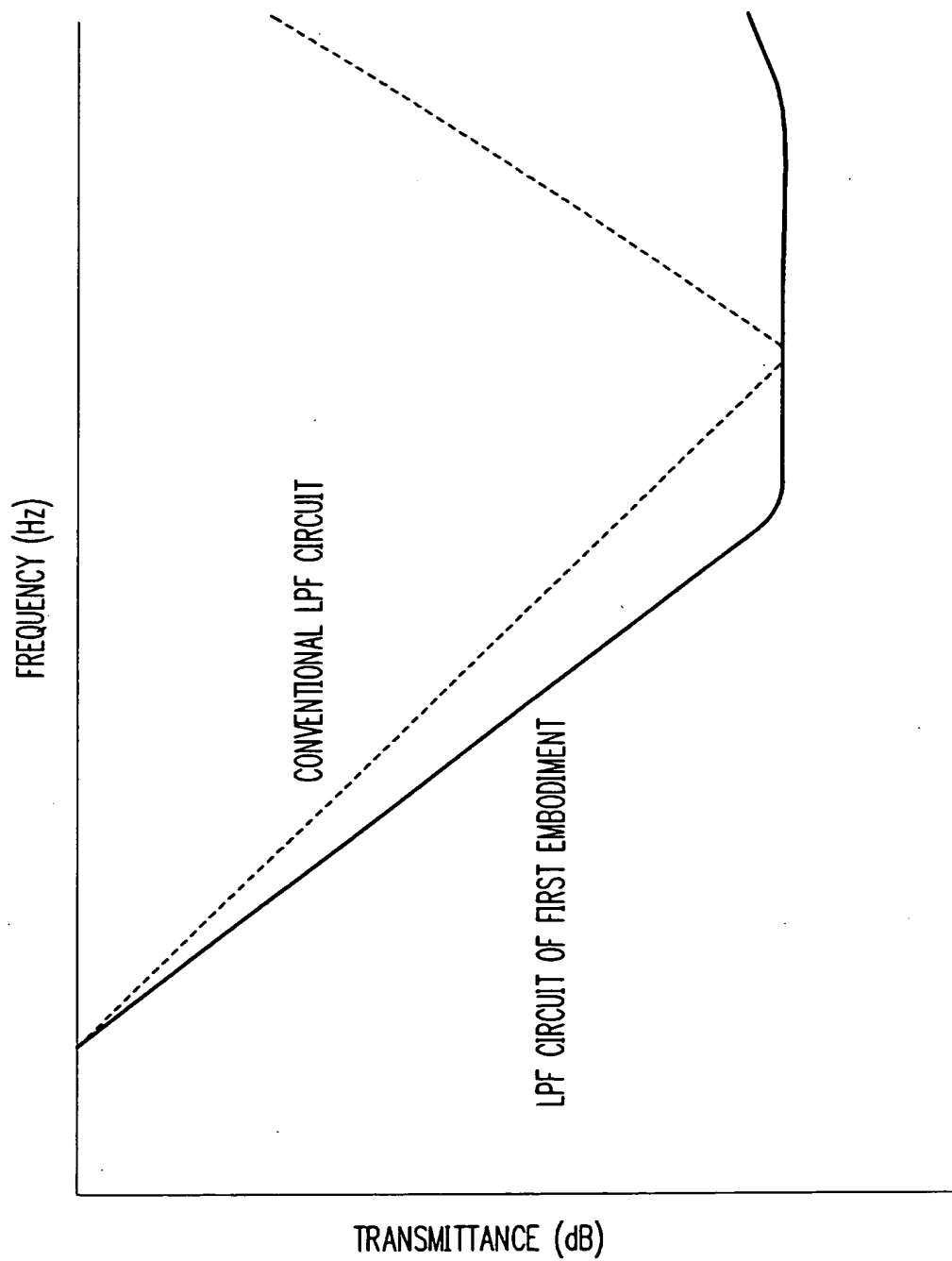


FIG. 12

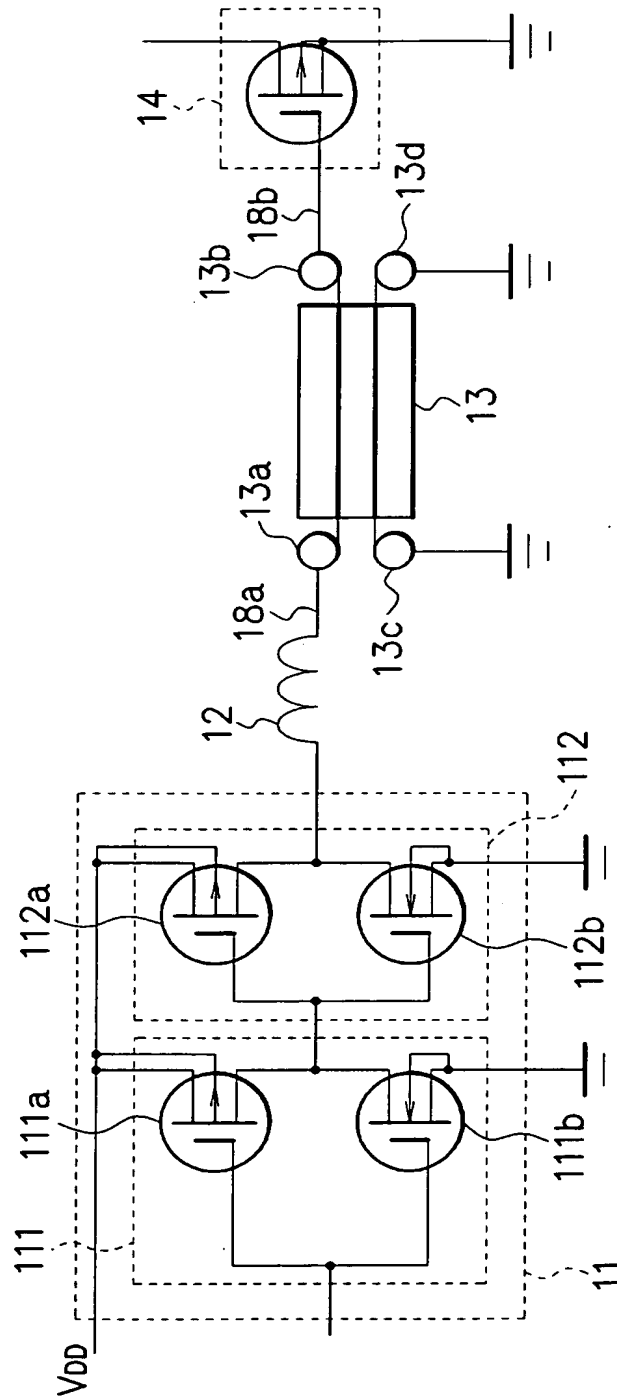


FIG. 13

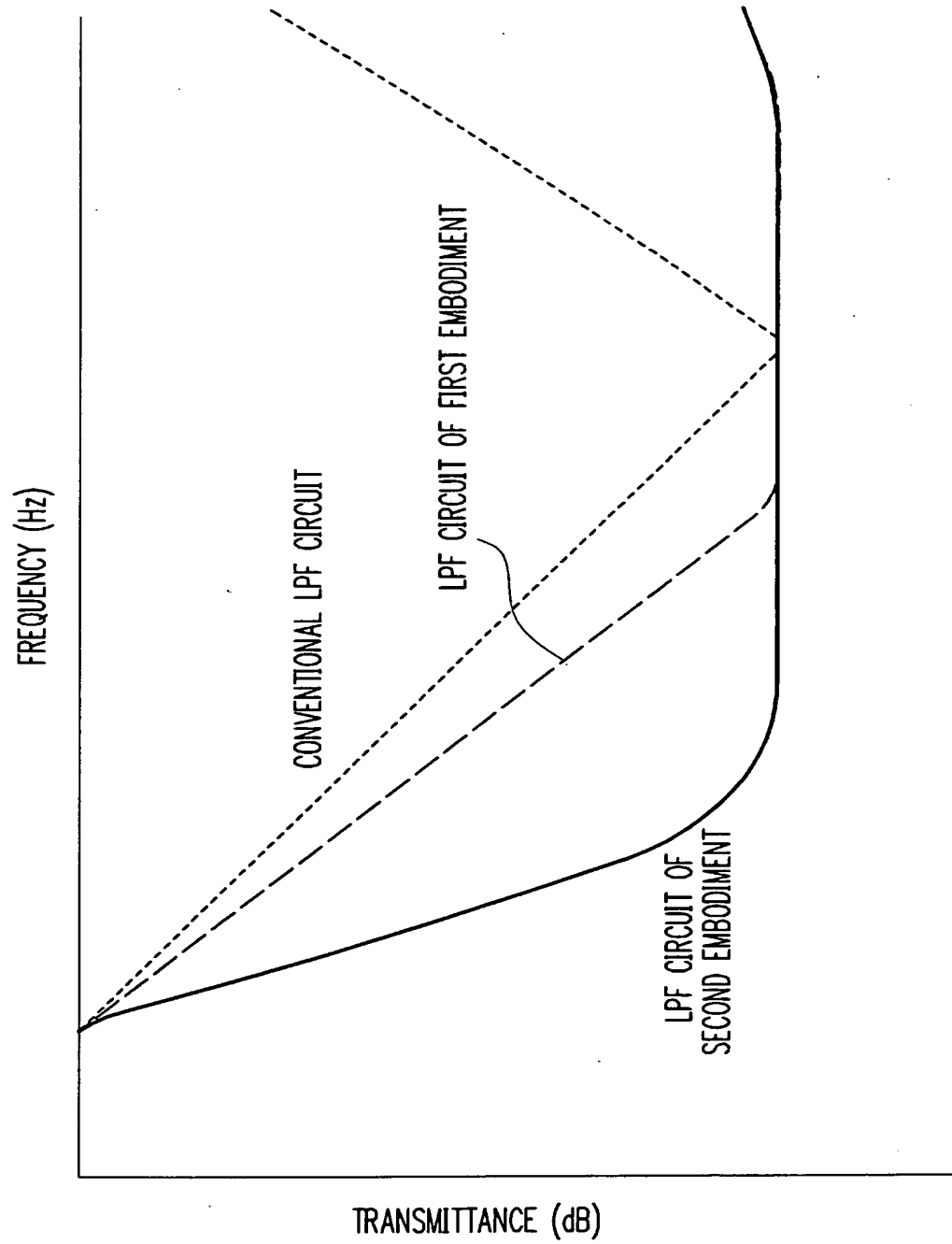
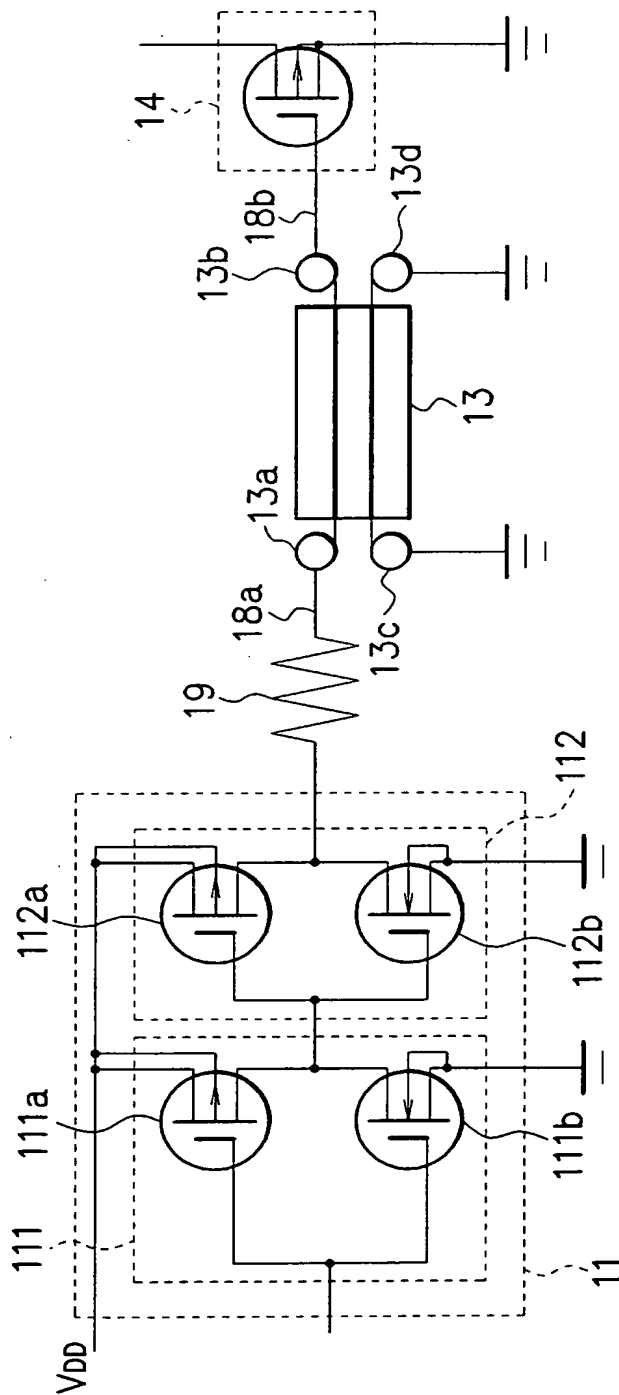
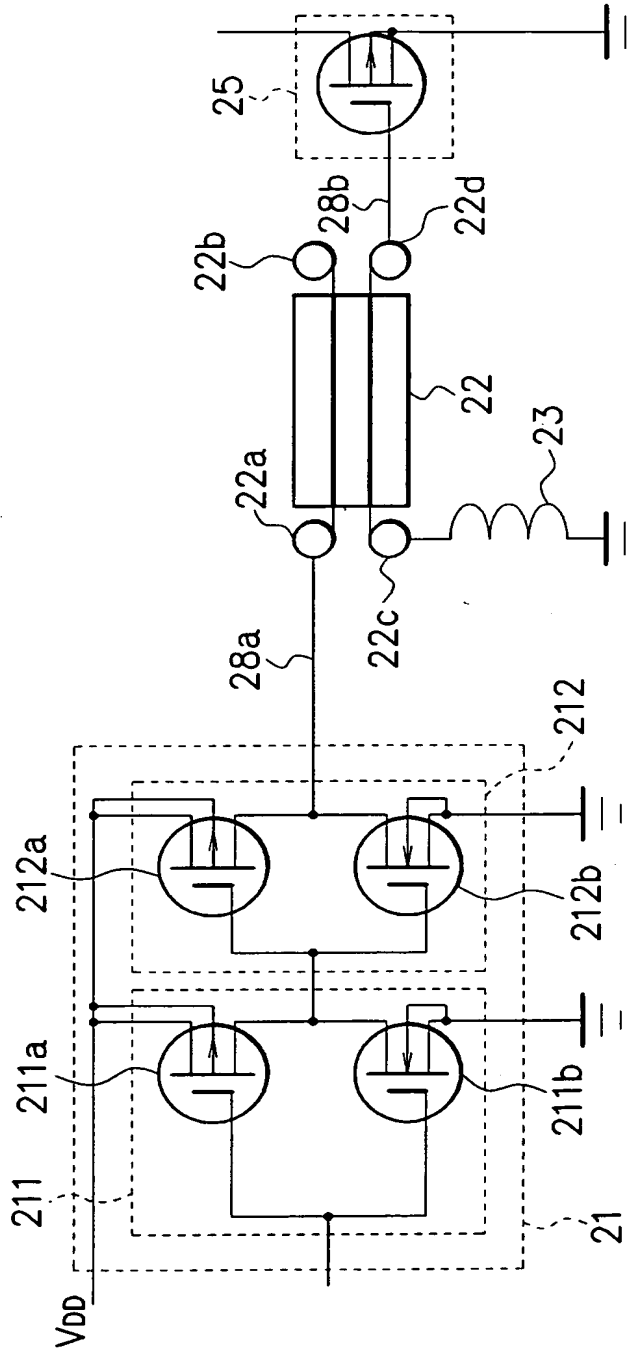


FIG. 14



F I G. 15



F I G. 16

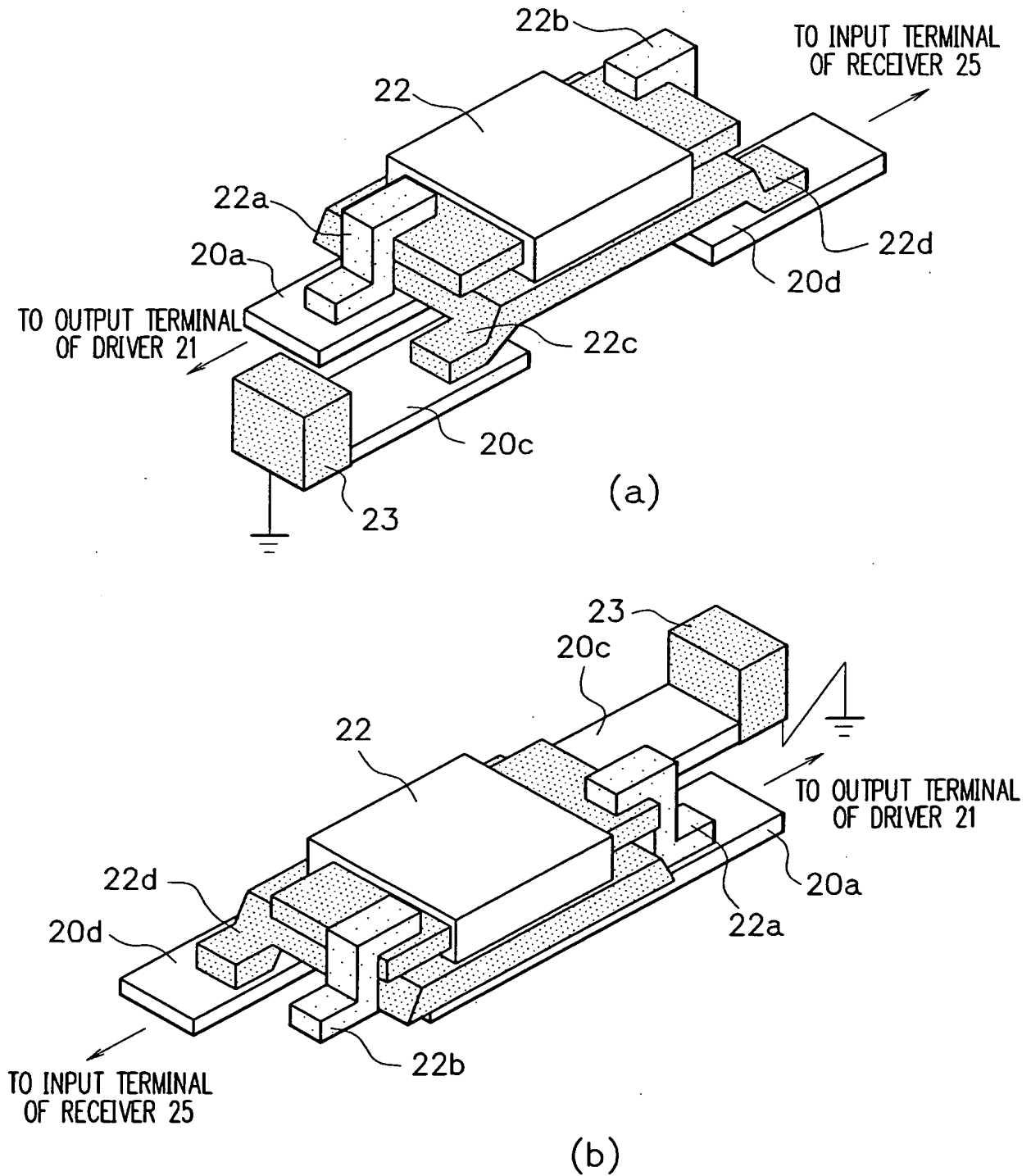


FIG. 17

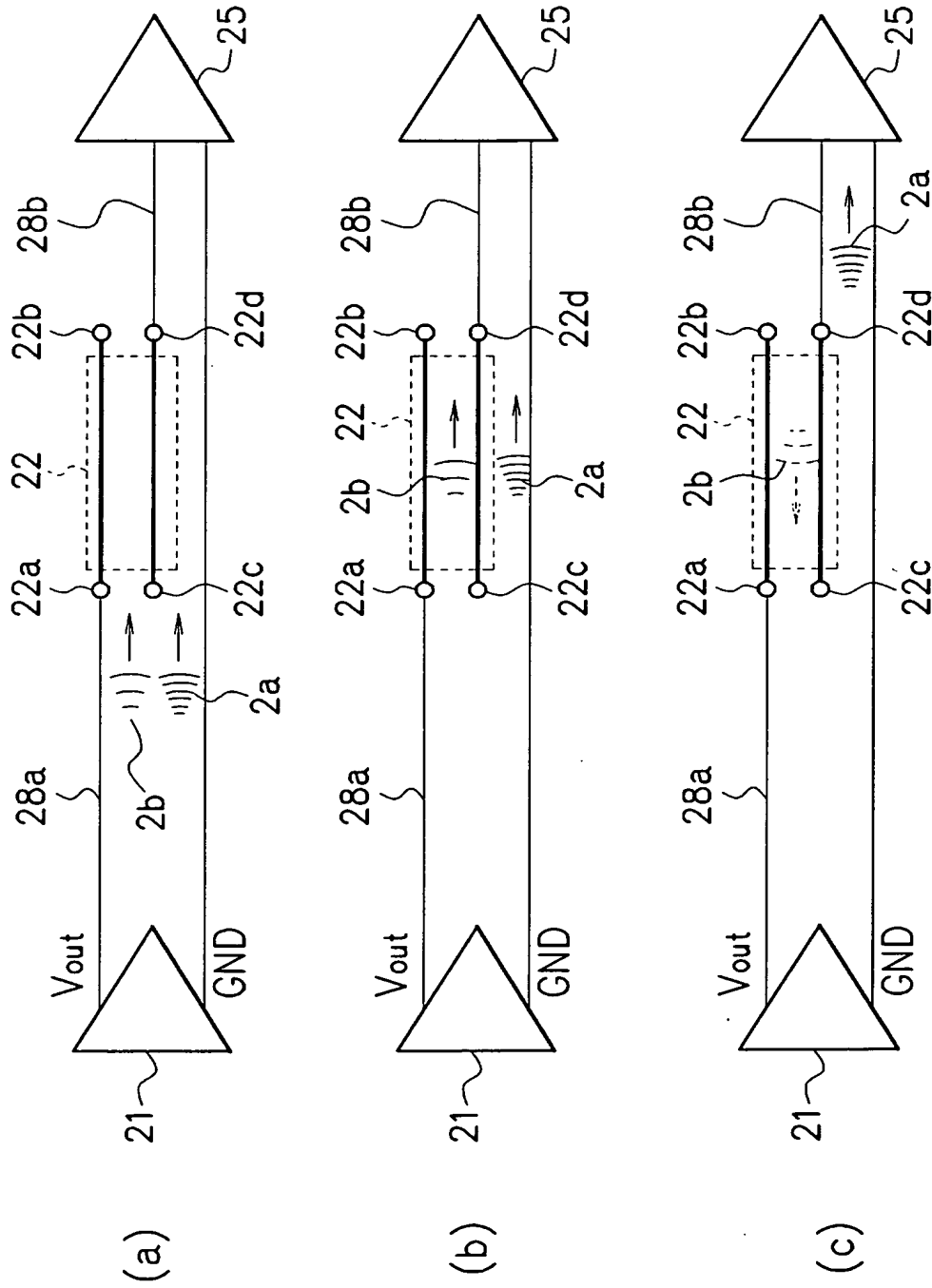


FIG. 18

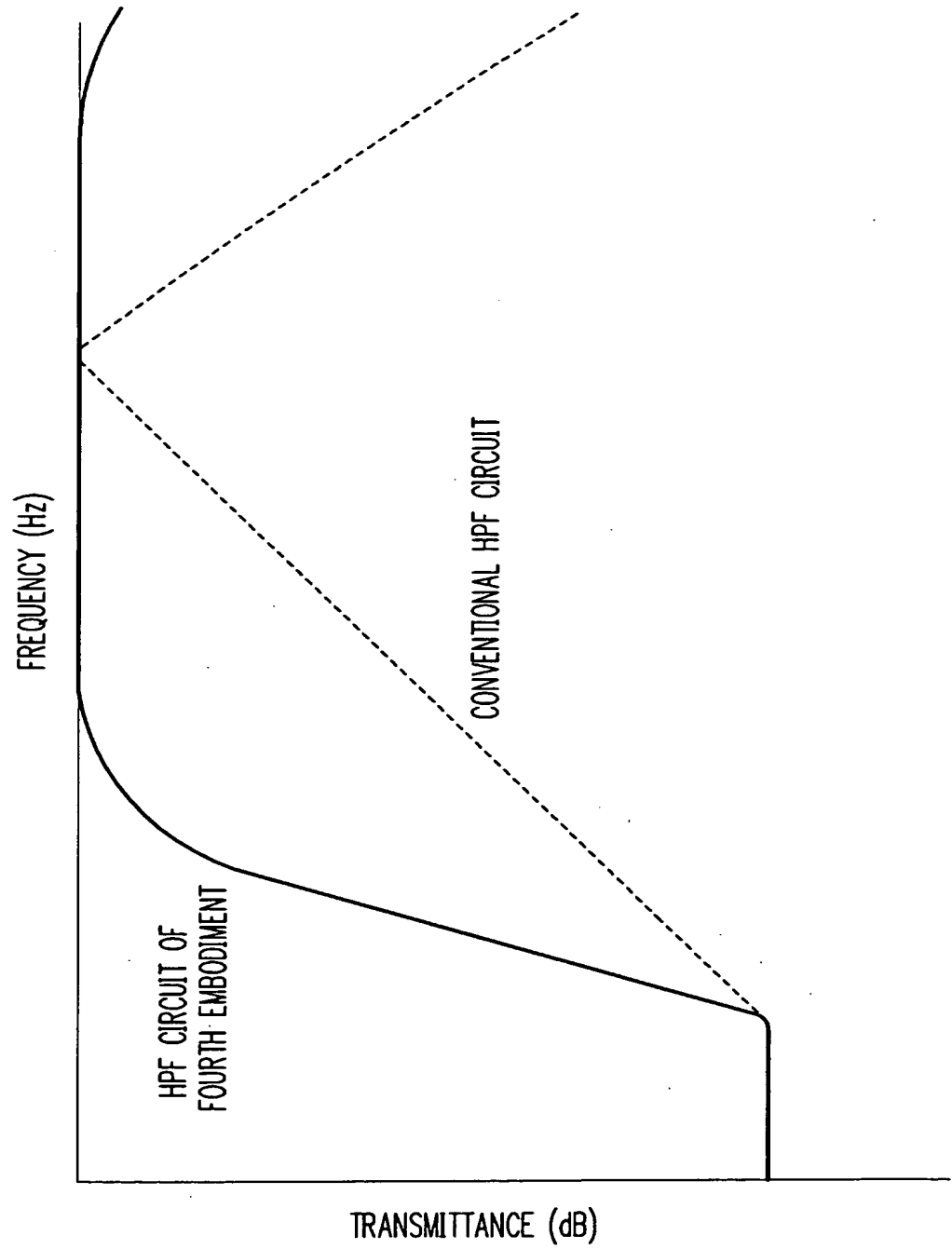
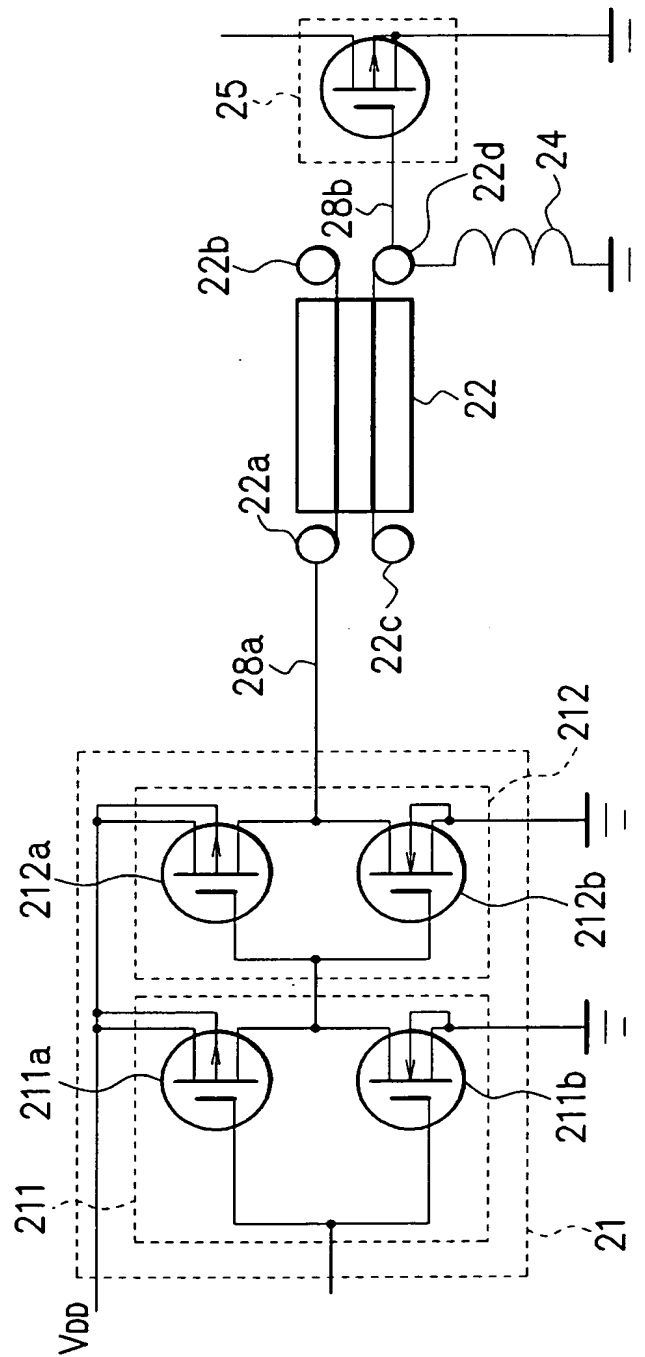


FIG. 19



F I G. 20

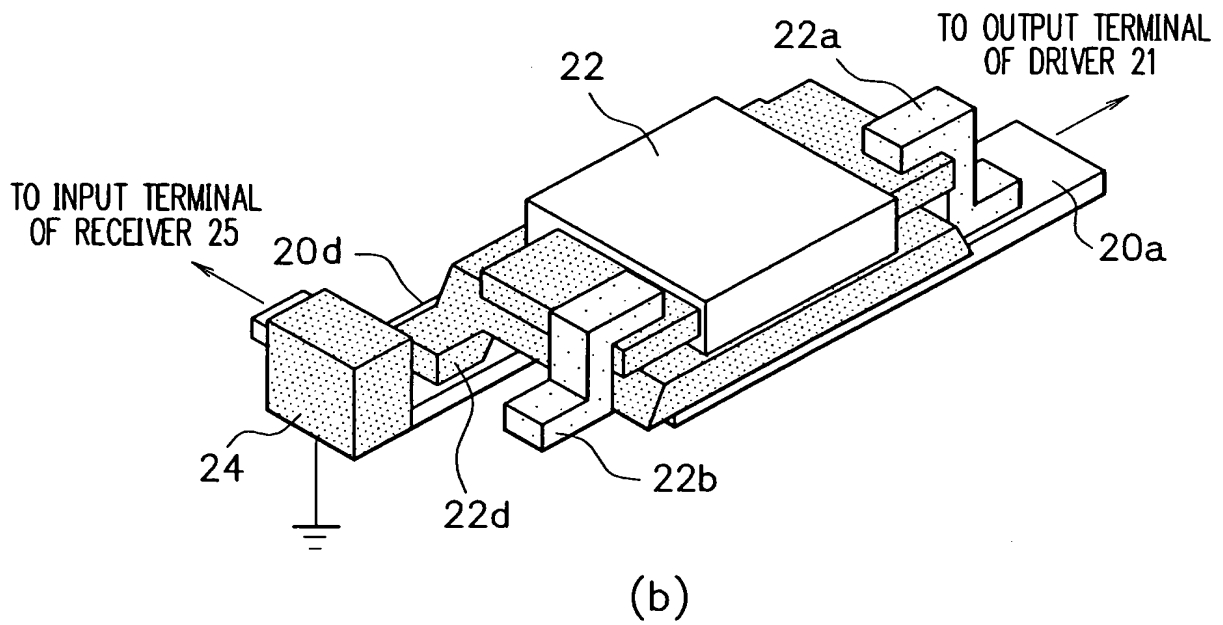
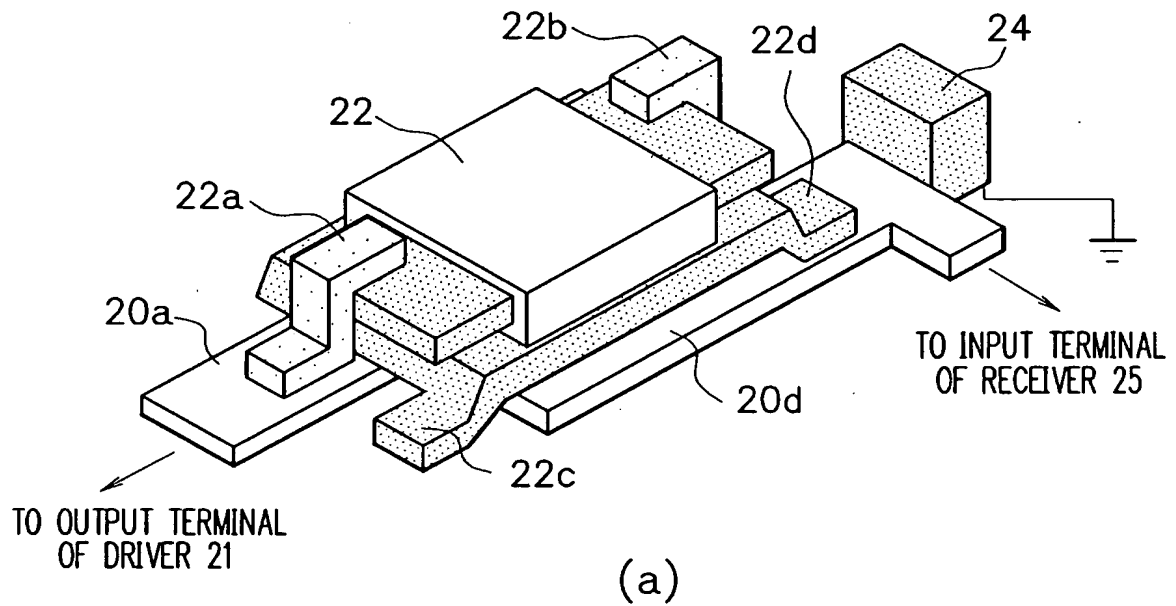


FIG. 21

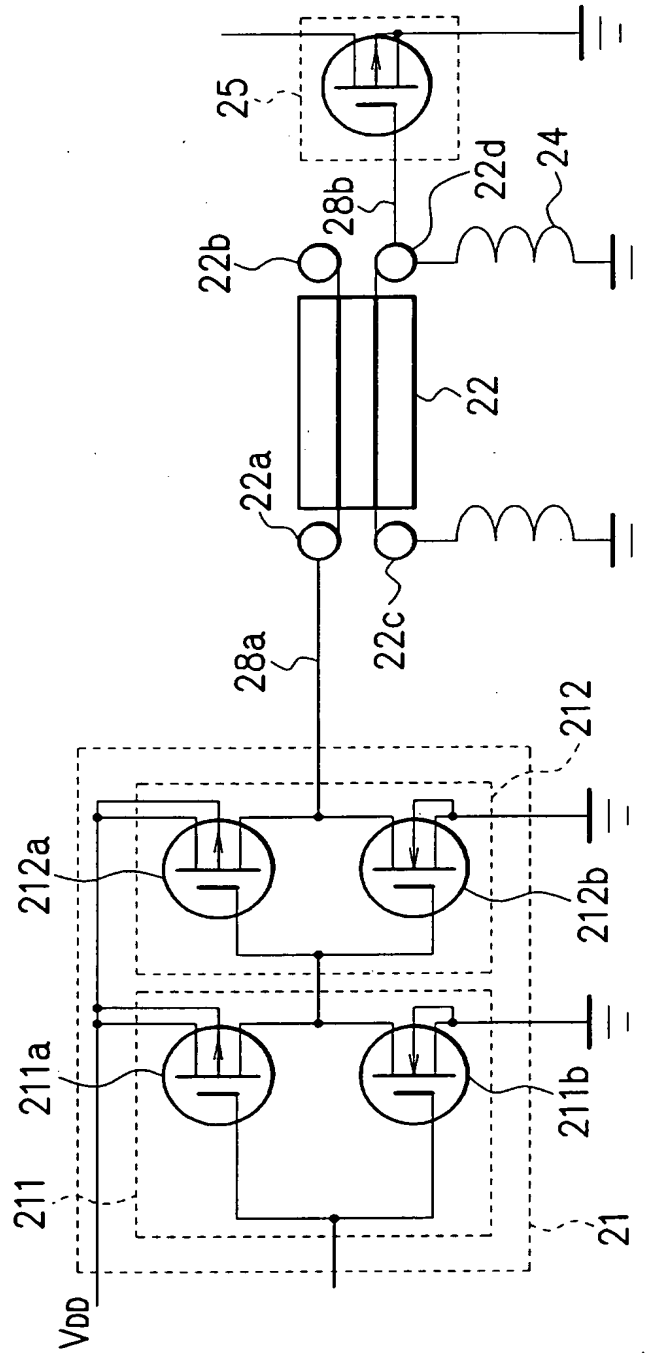


FIG. 22

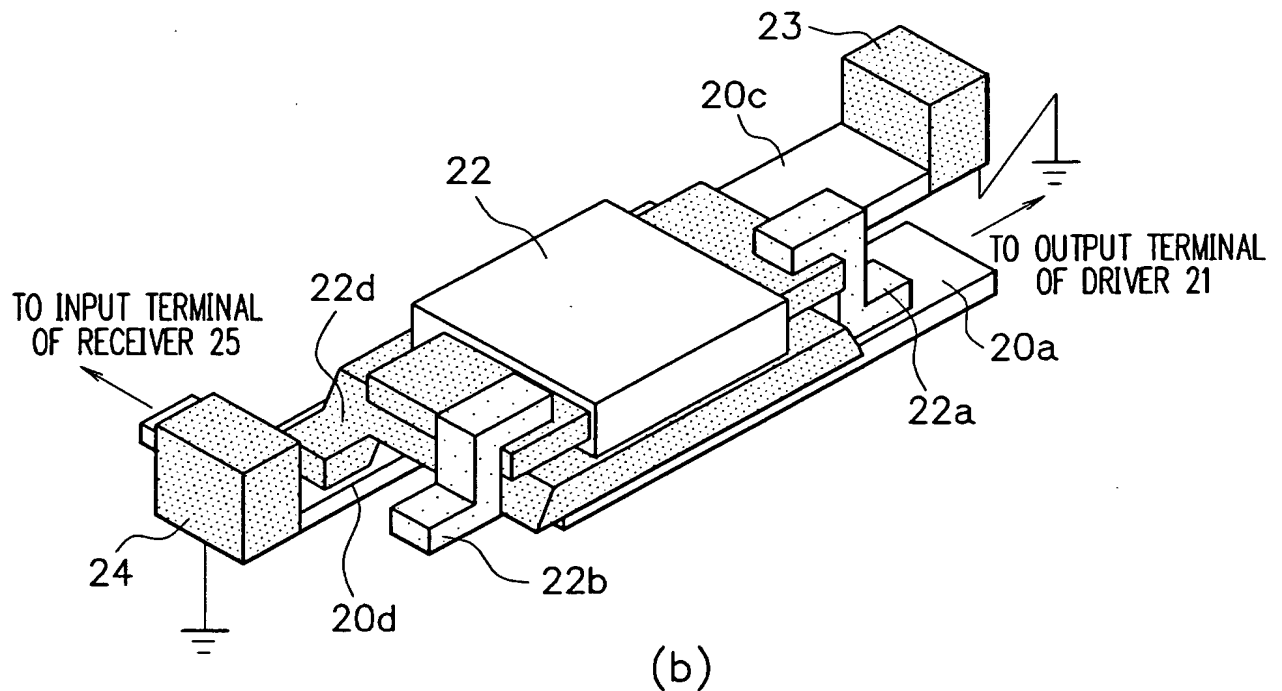
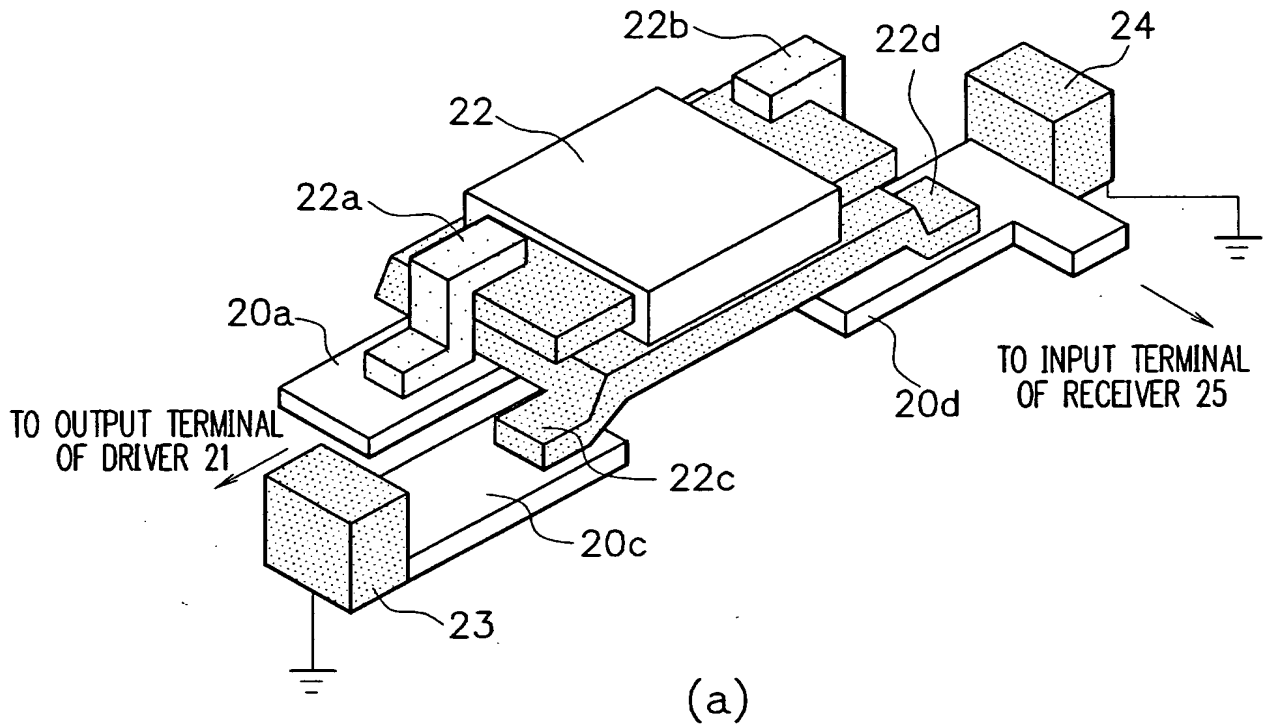


FIG. 23

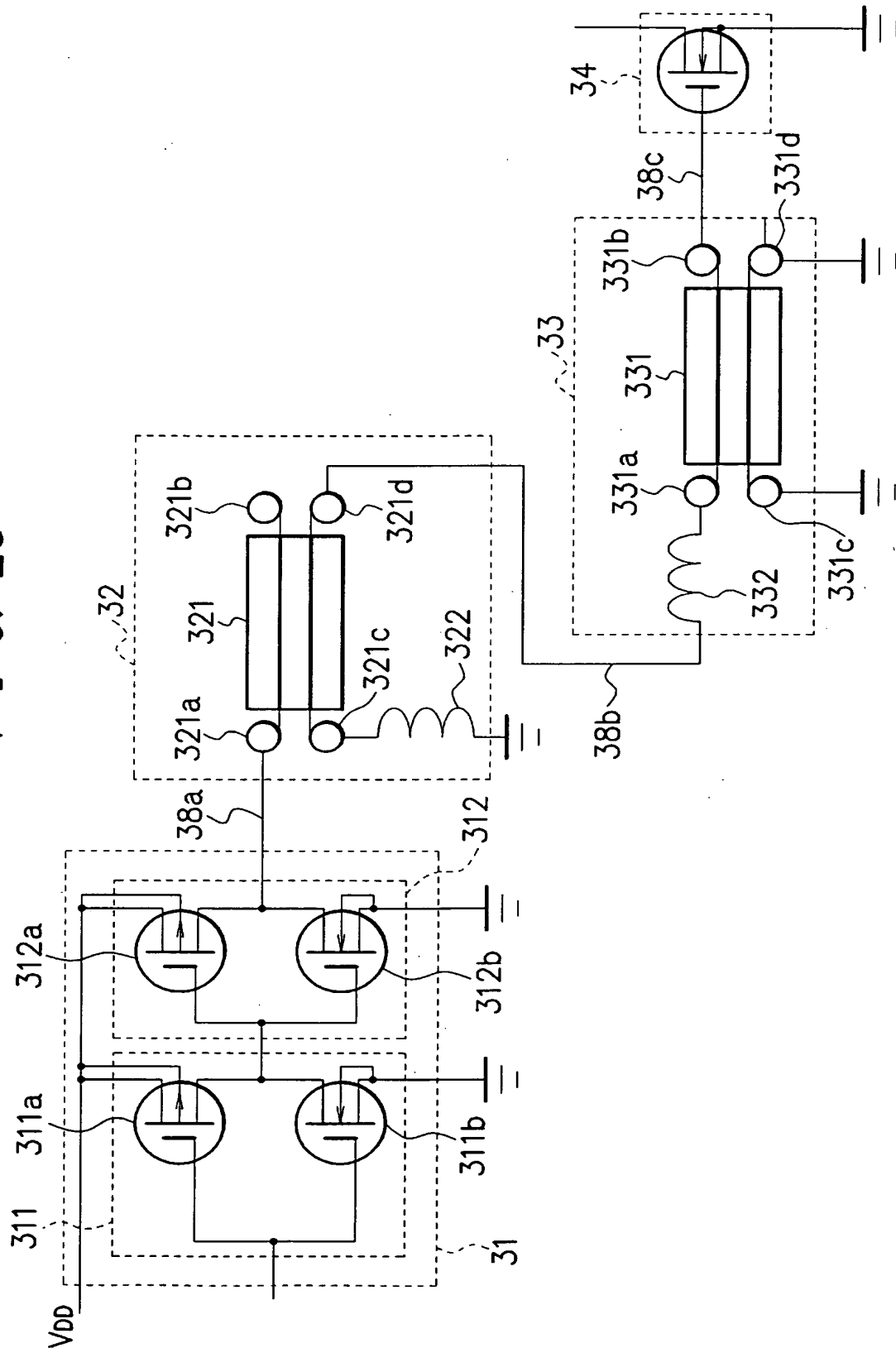


FIG. 24

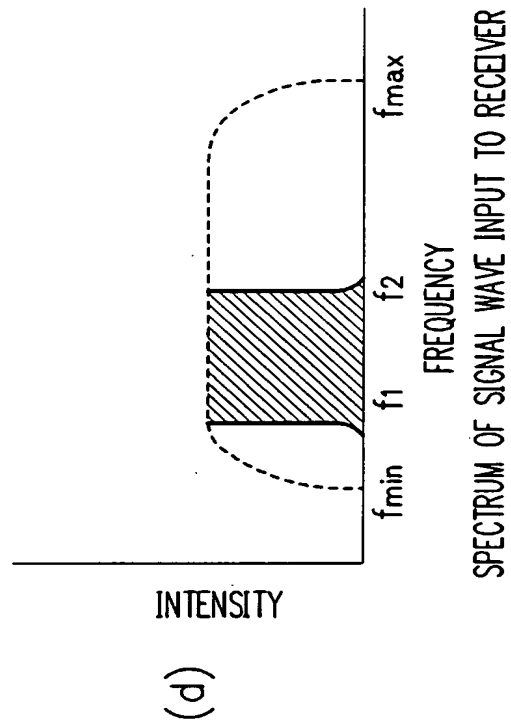
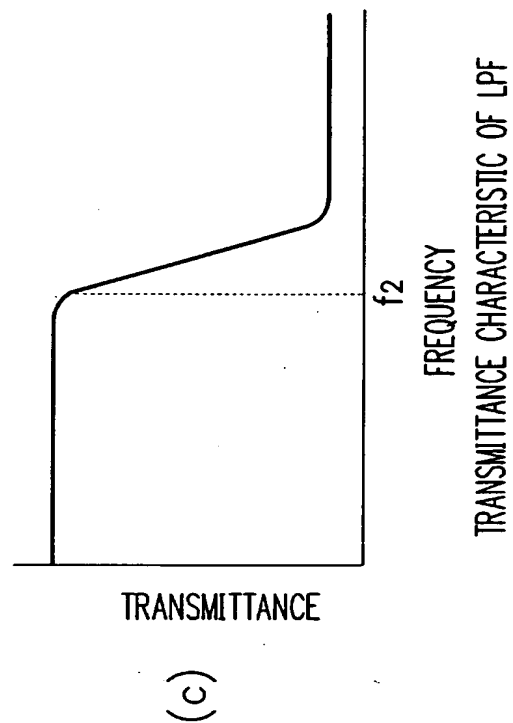
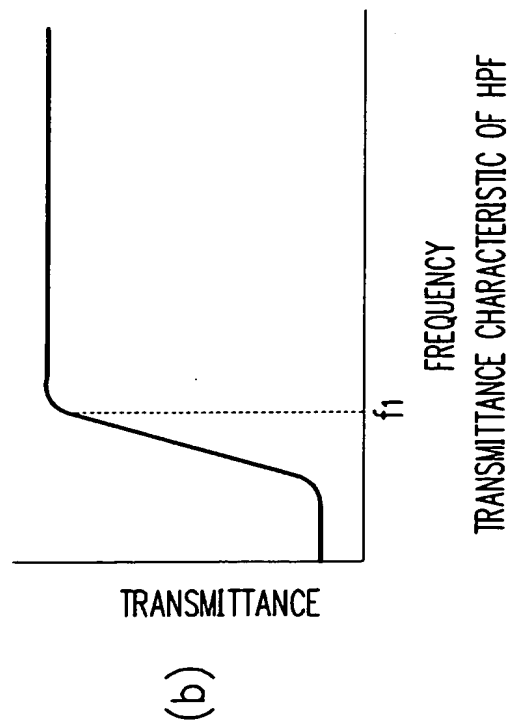
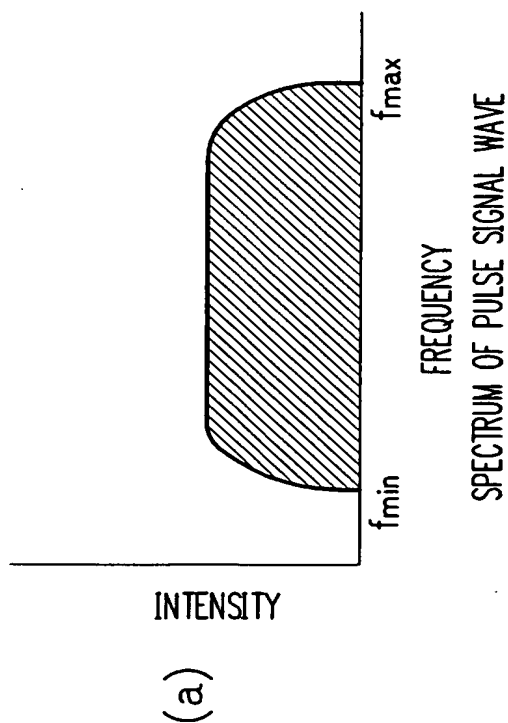


FIG. 25

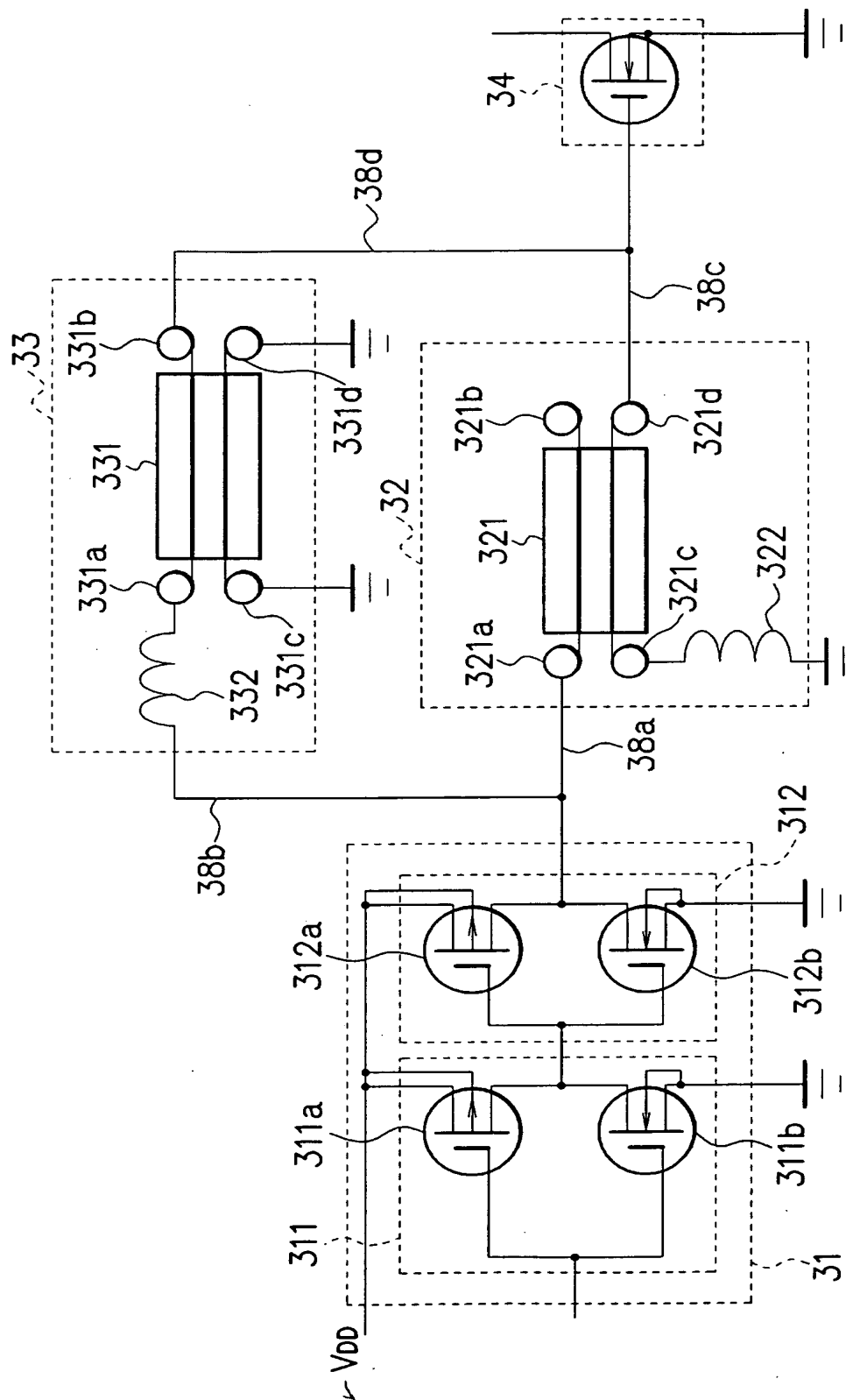


FIG. 26

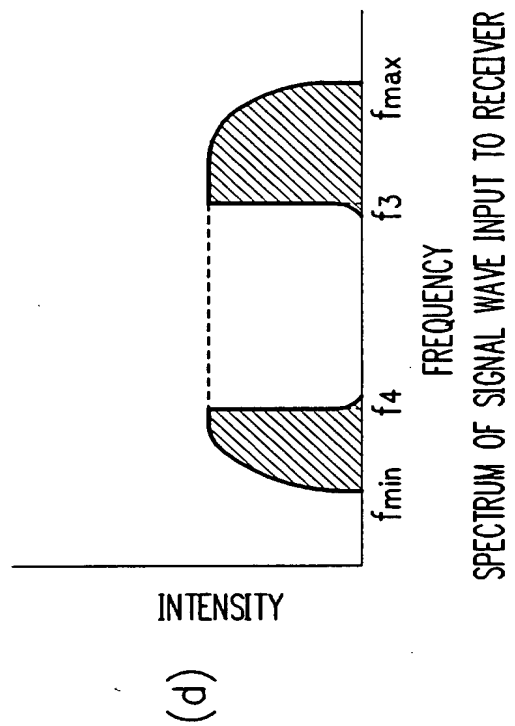
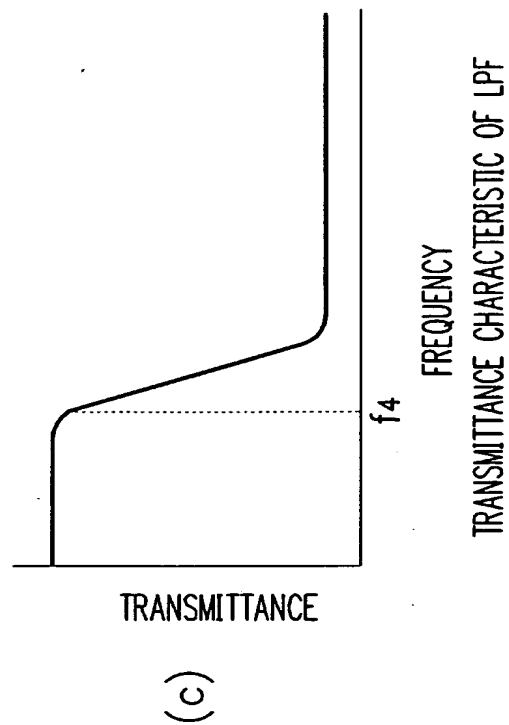
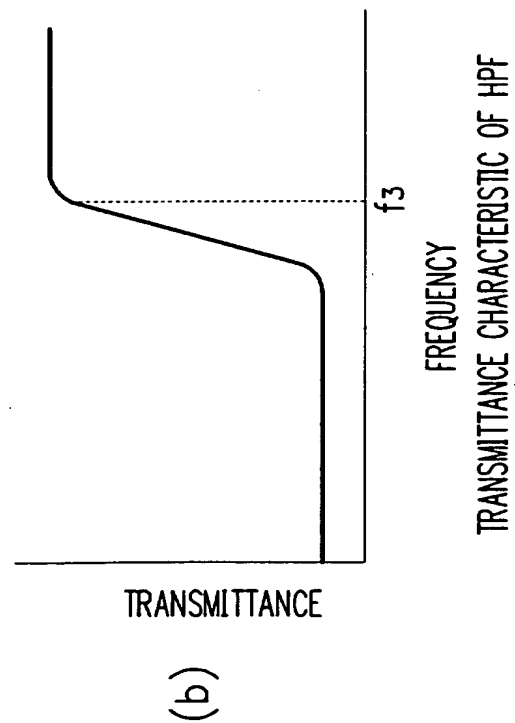
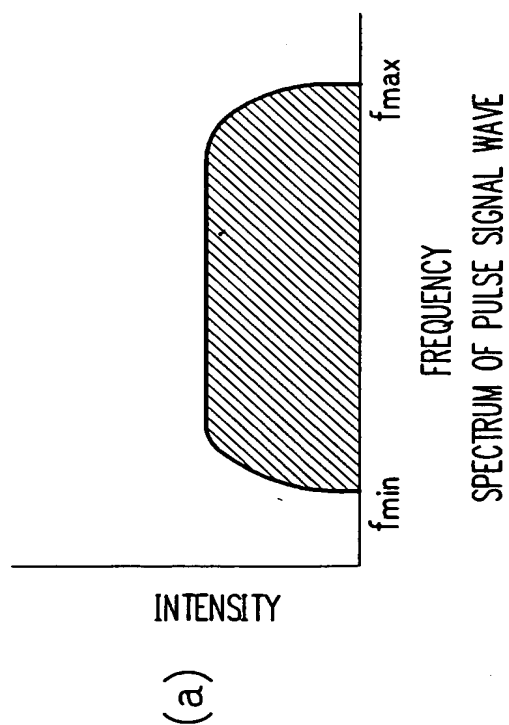
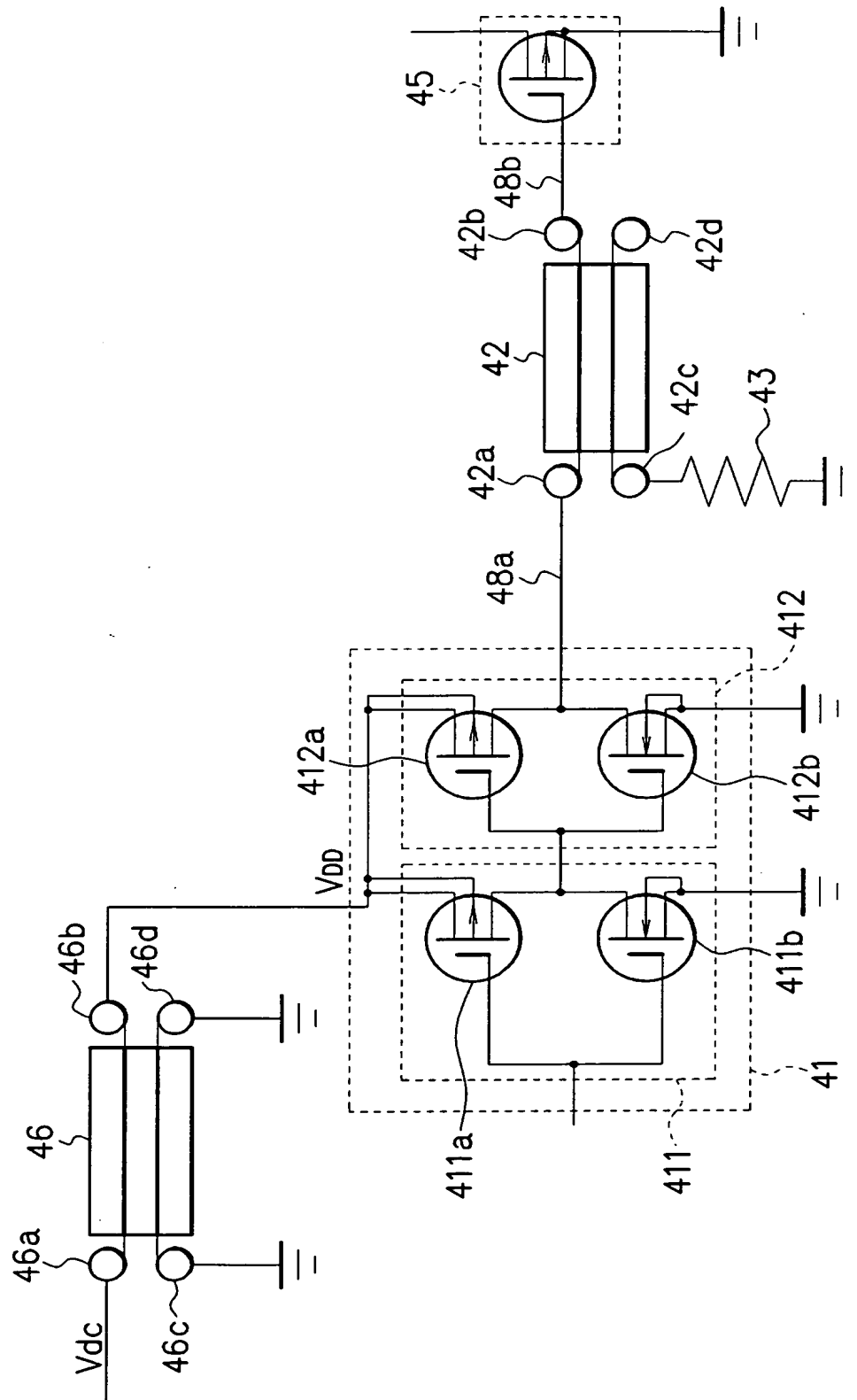


FIG. 27



F I G. 28

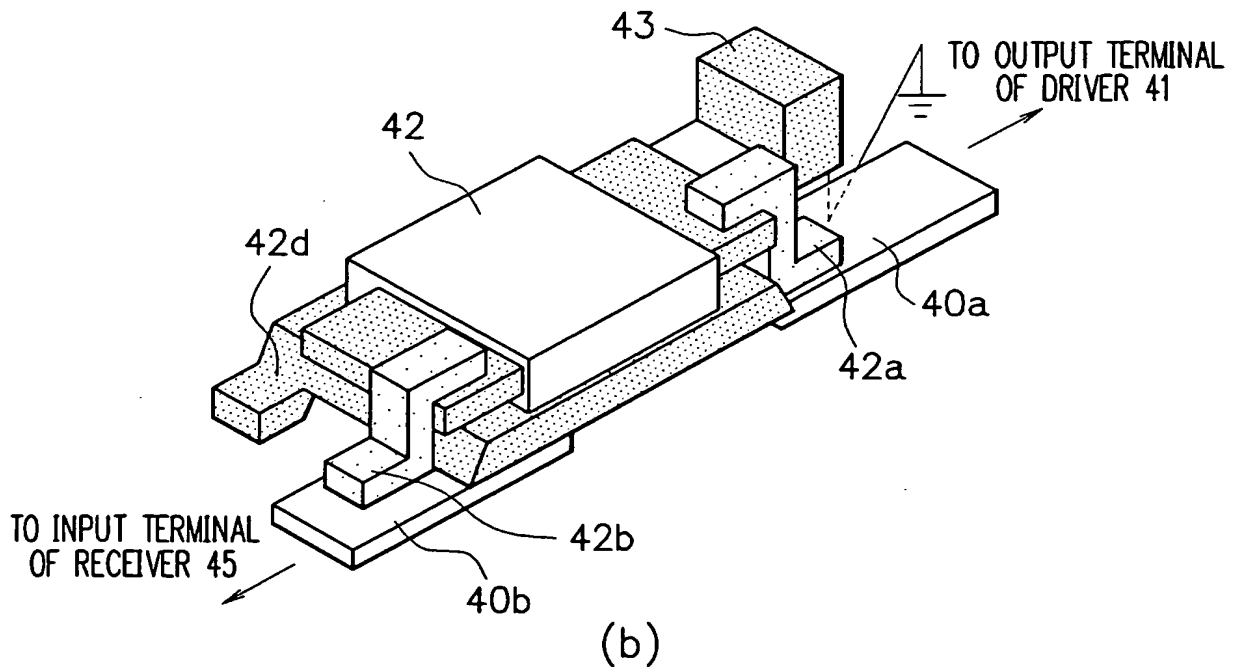
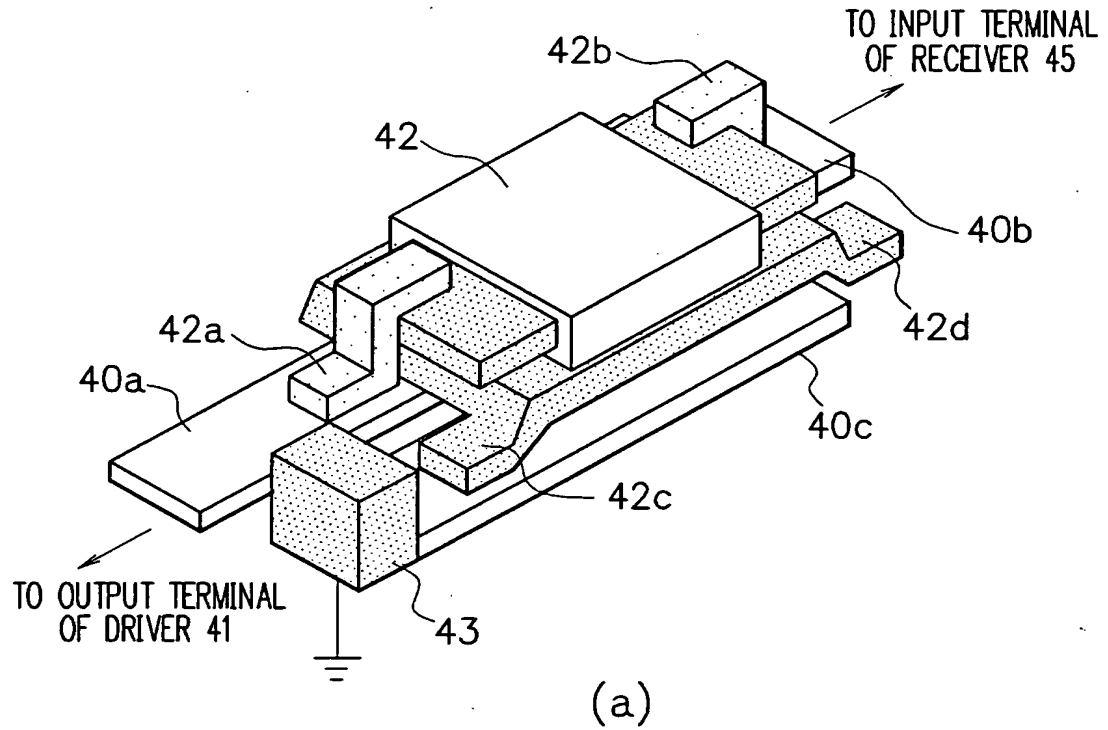
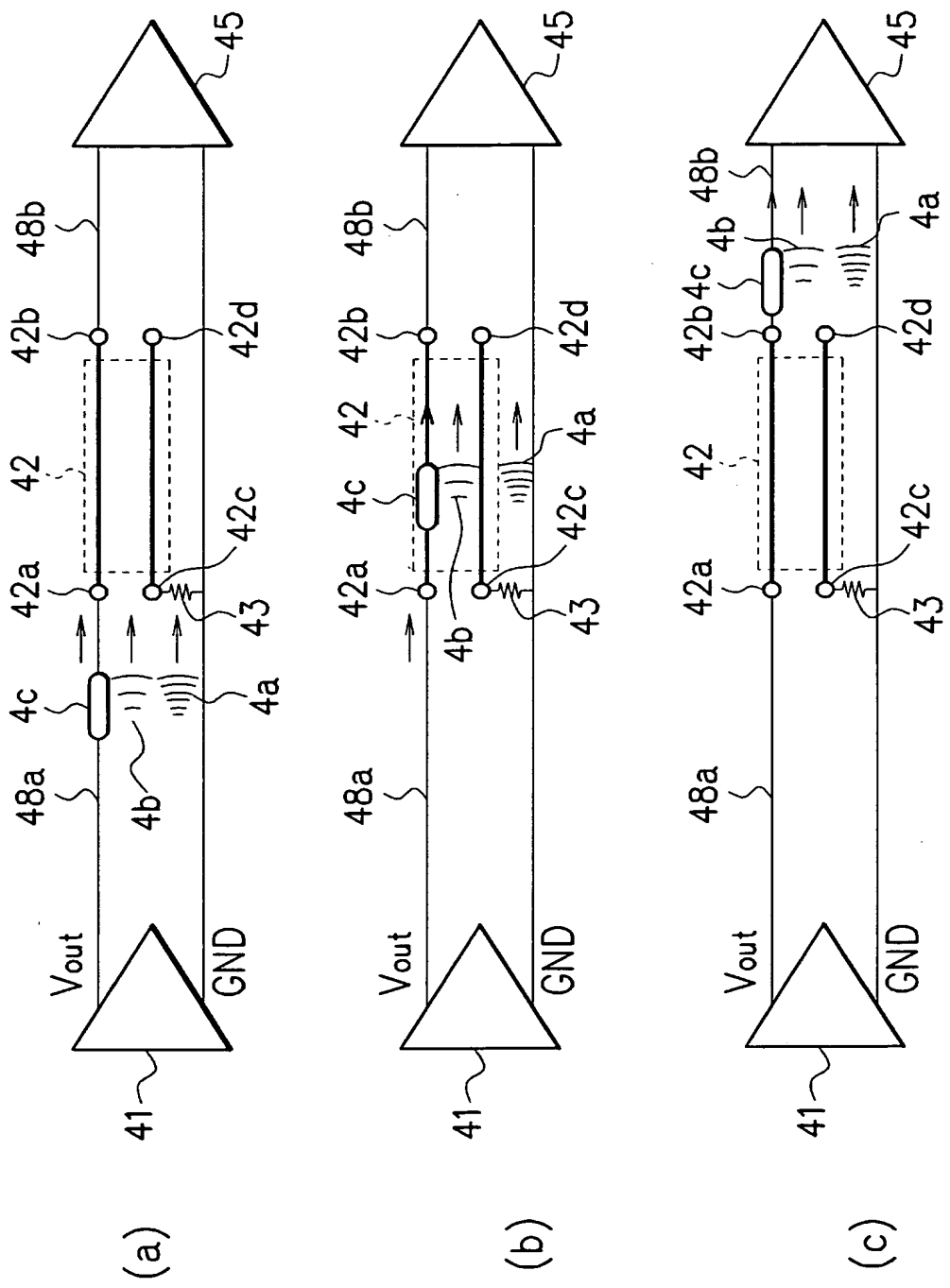


FIG. 29



F I G. 30

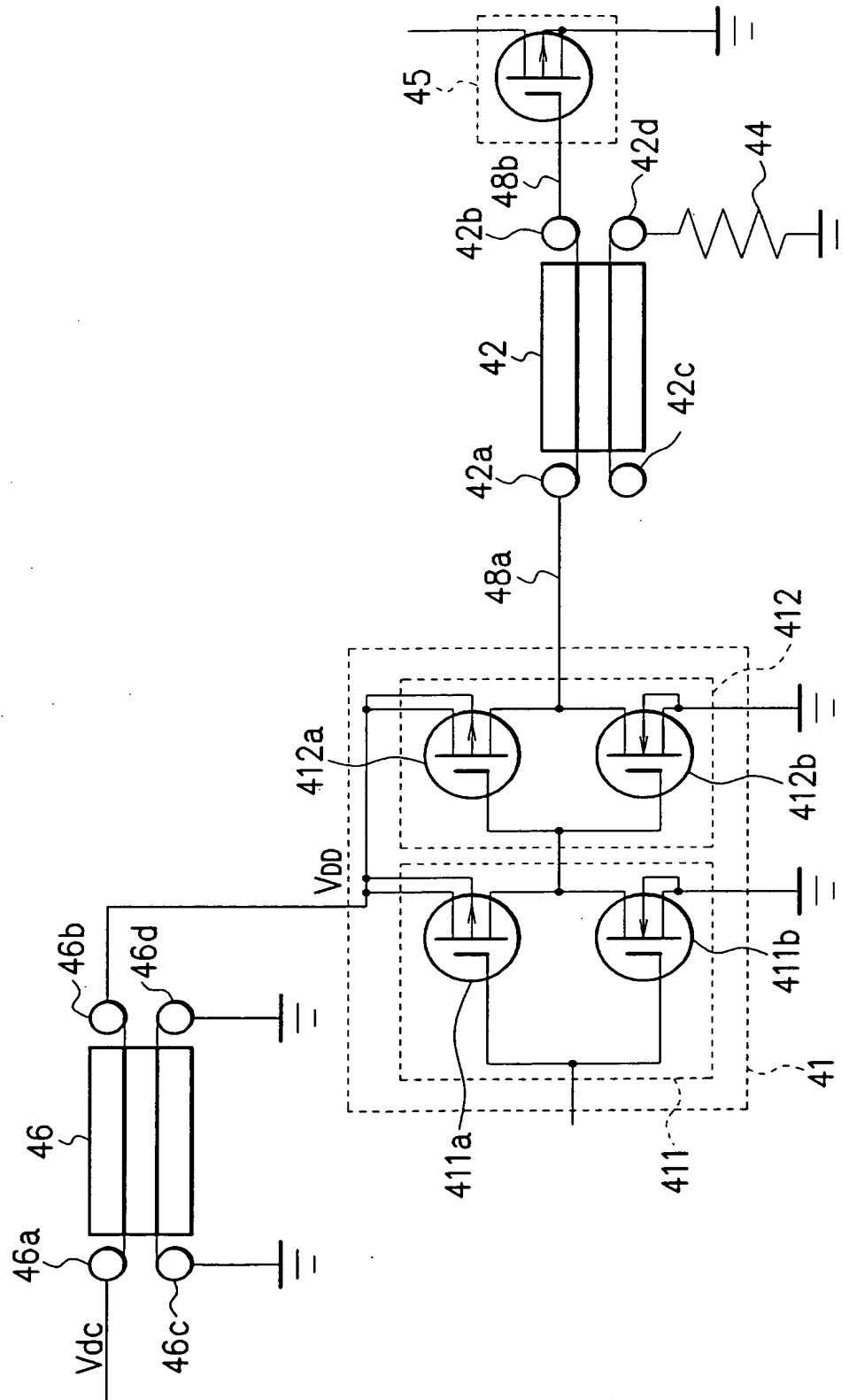
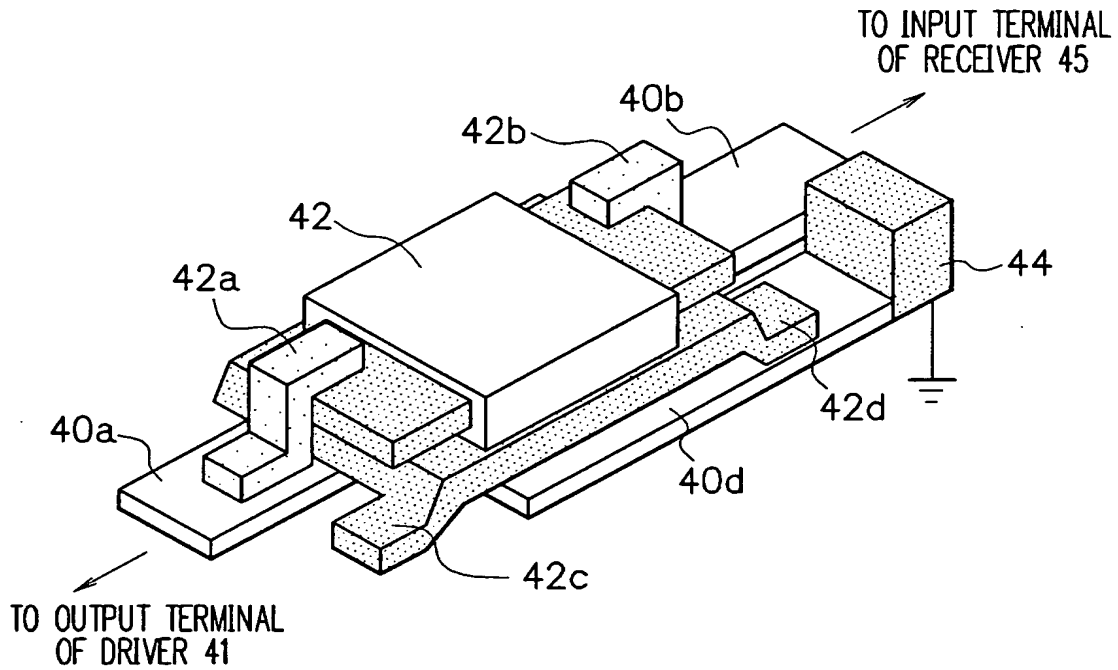
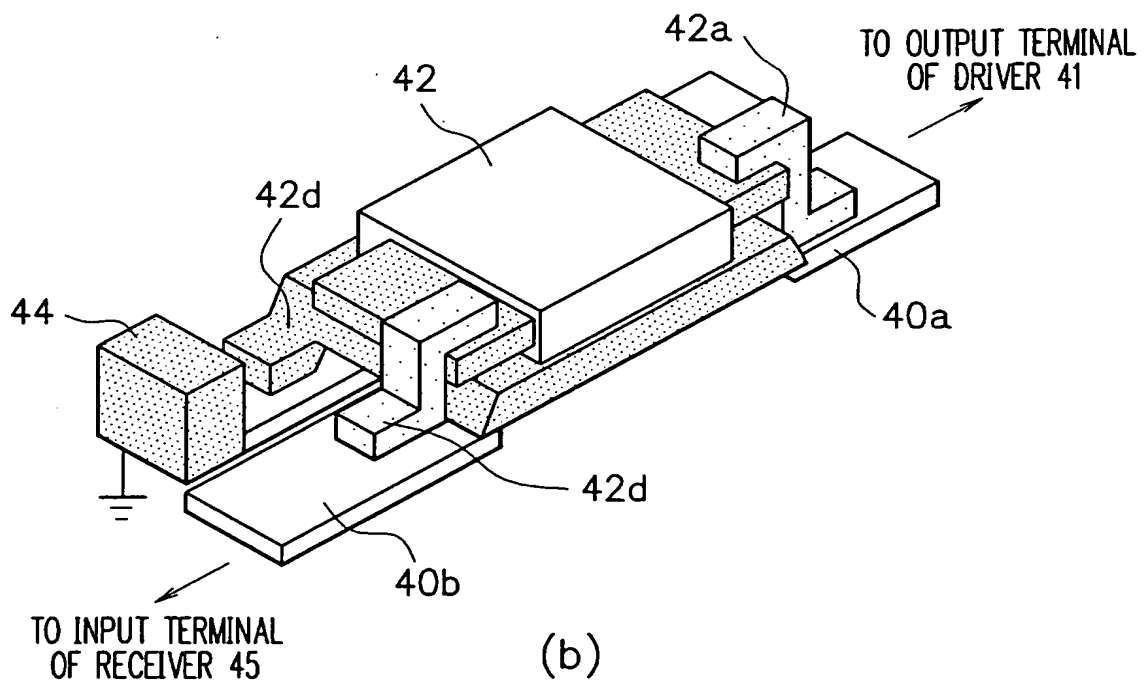


FIG. 31

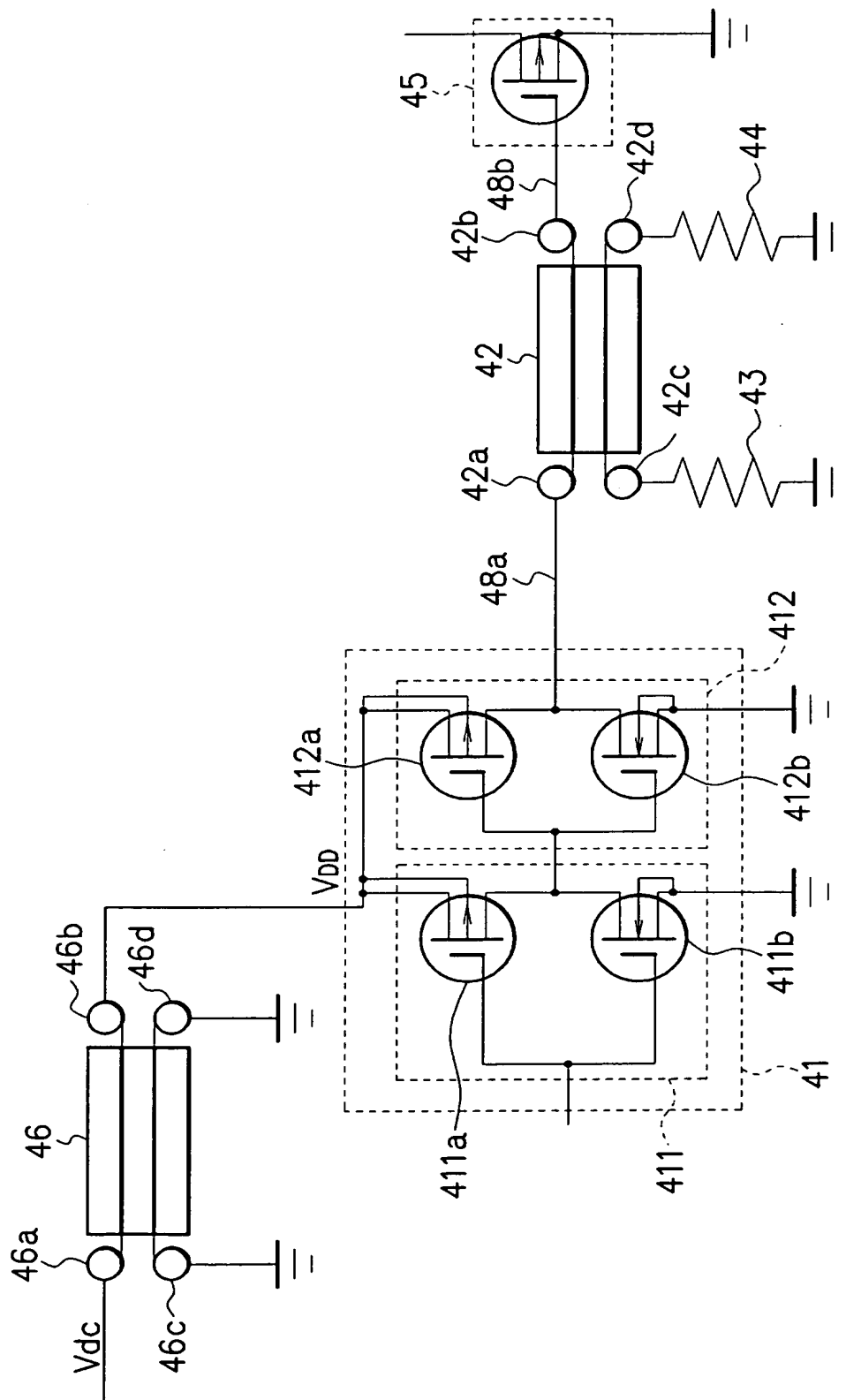


(a)



(b)

FIG. 32



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FIG. 33

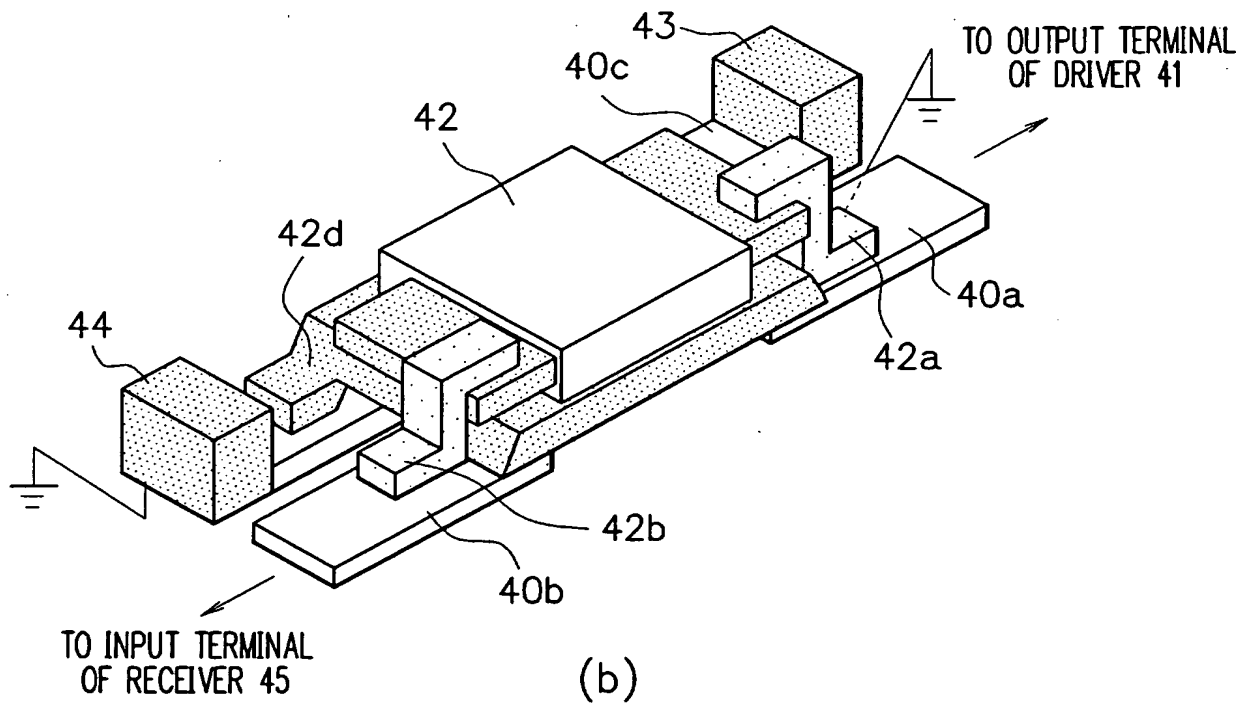
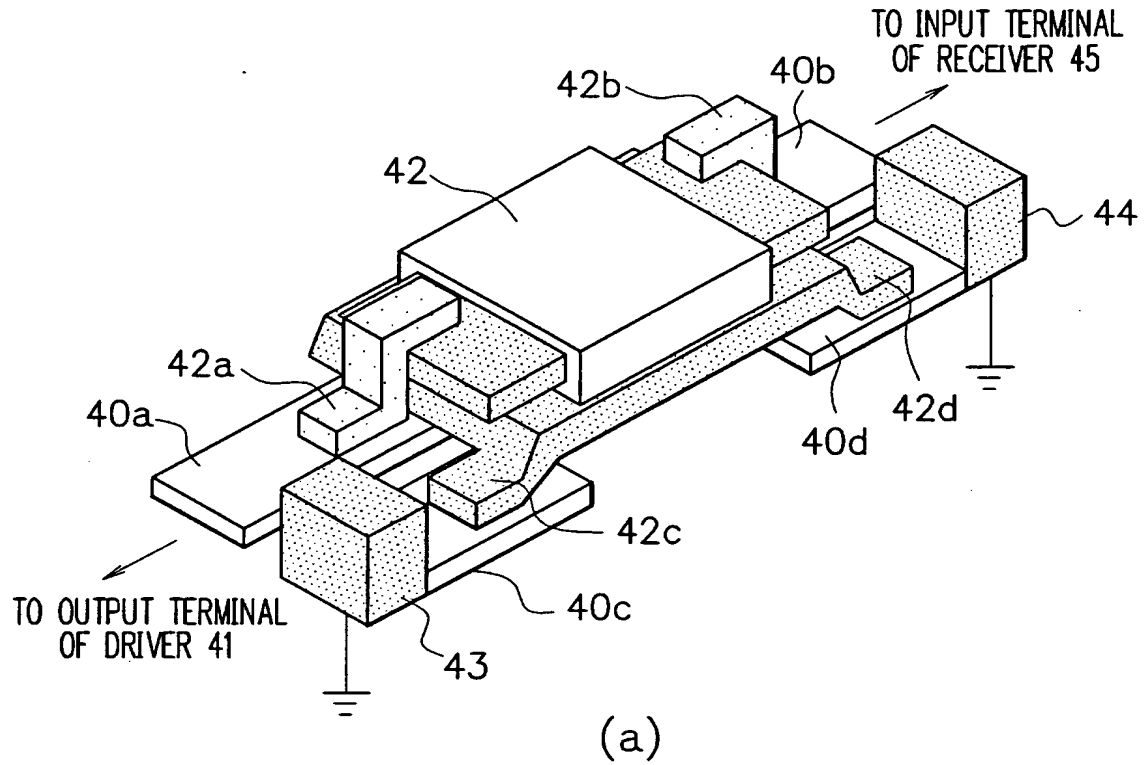


FIG. 34

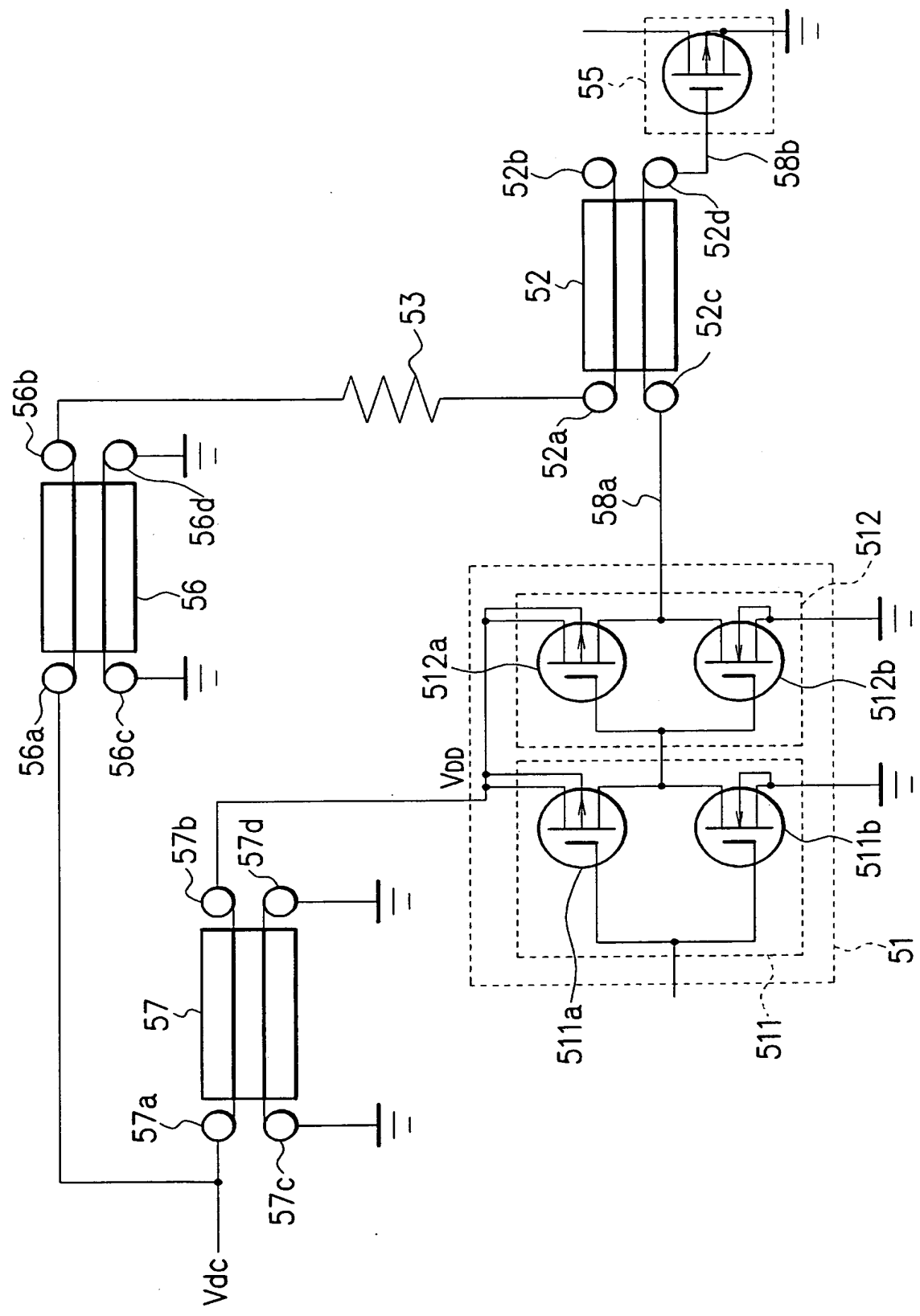
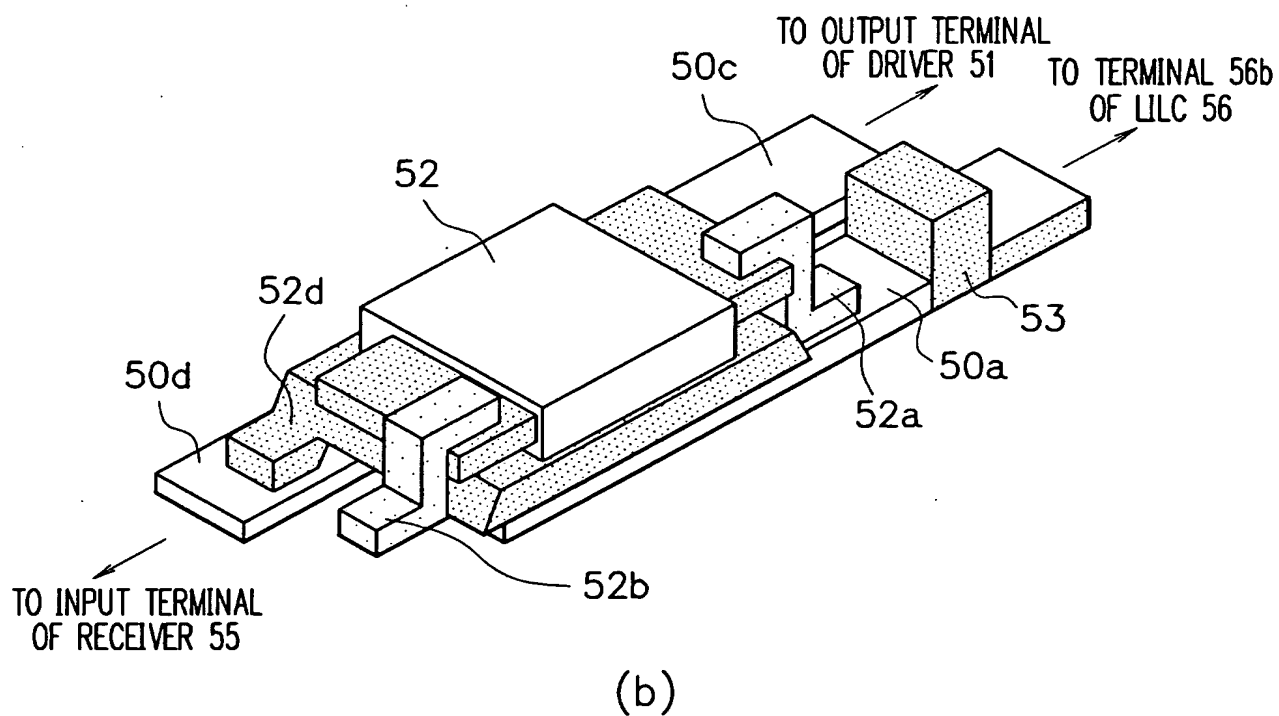
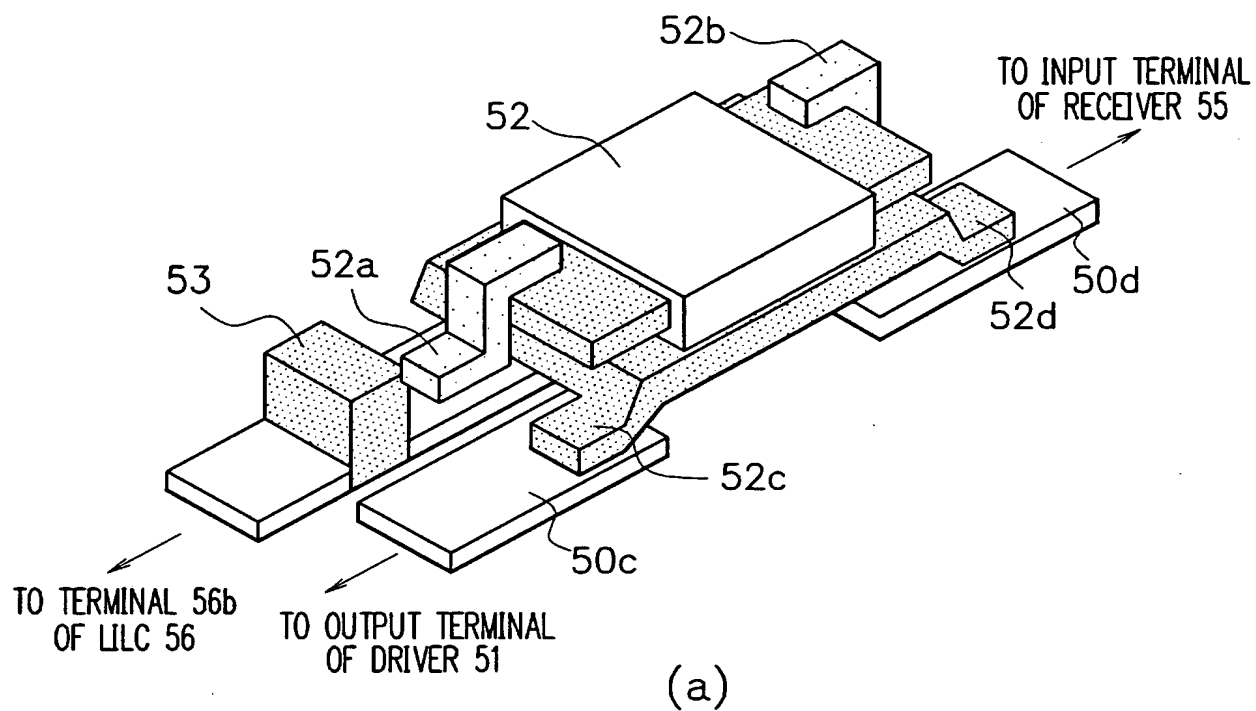
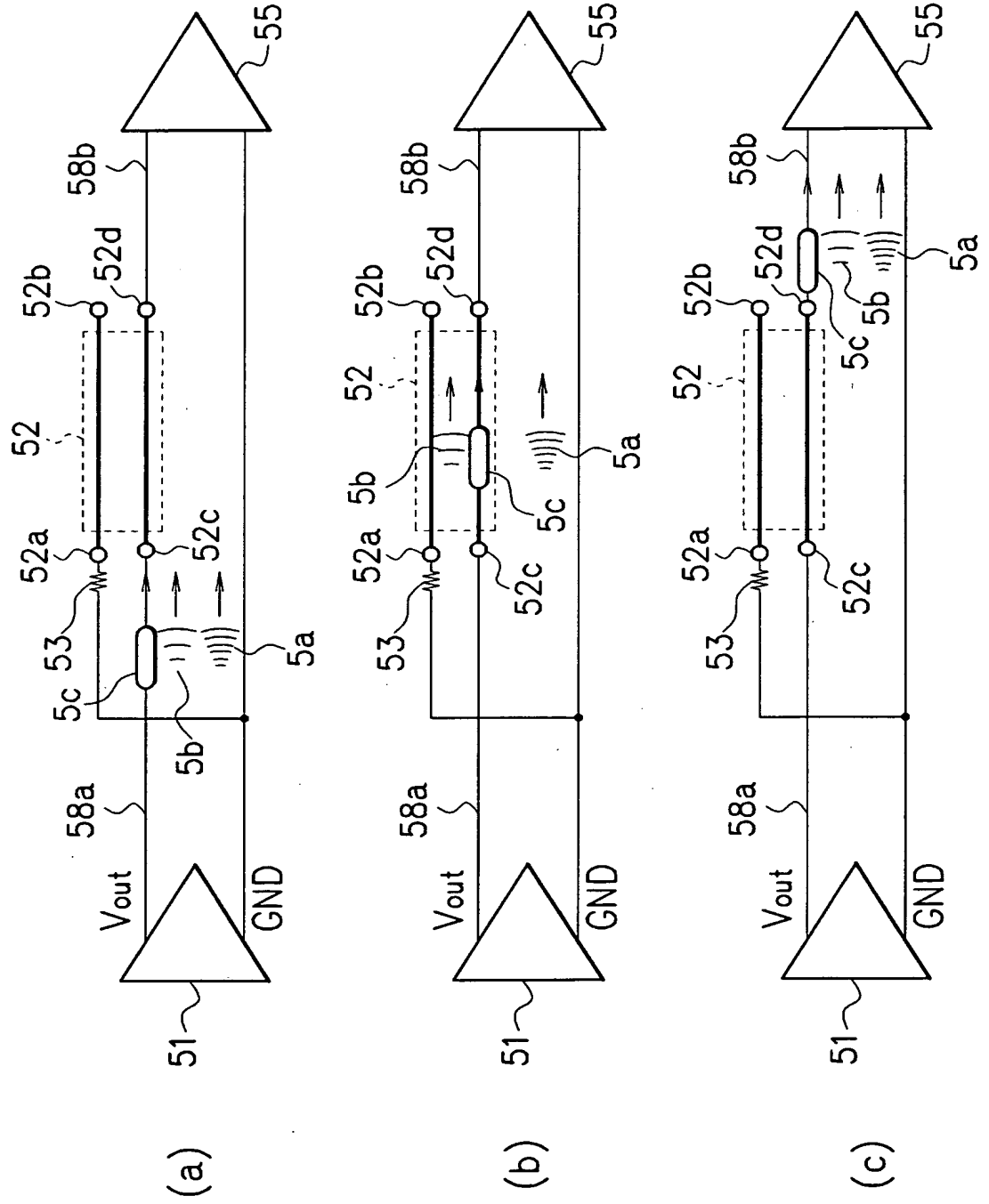


FIG. 35

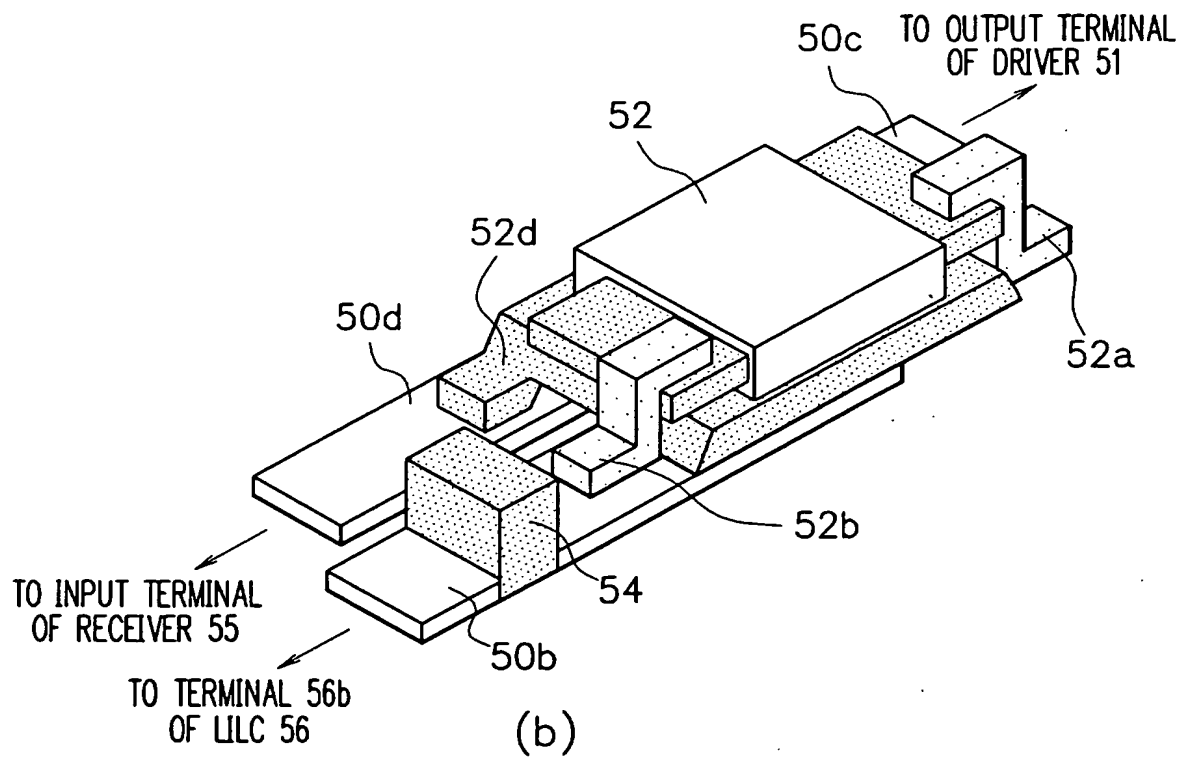
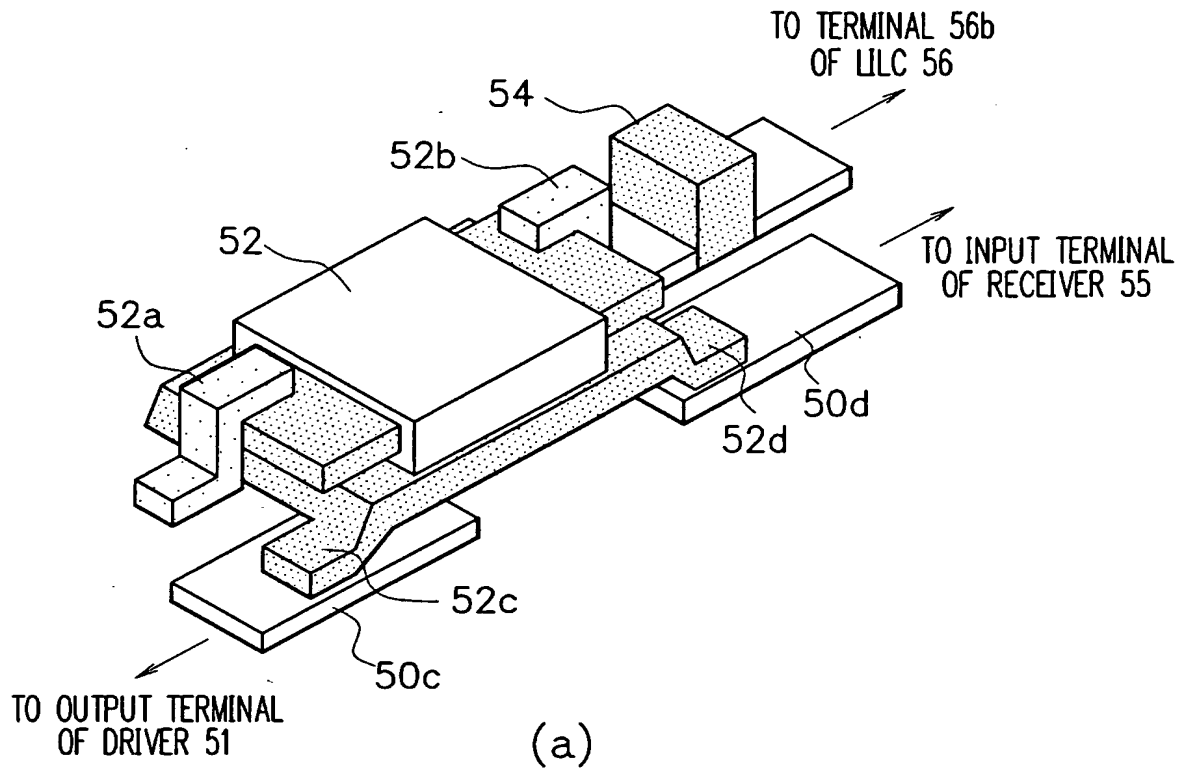


F I G. 36



[illegible]

FIG. 38



F I G. 39

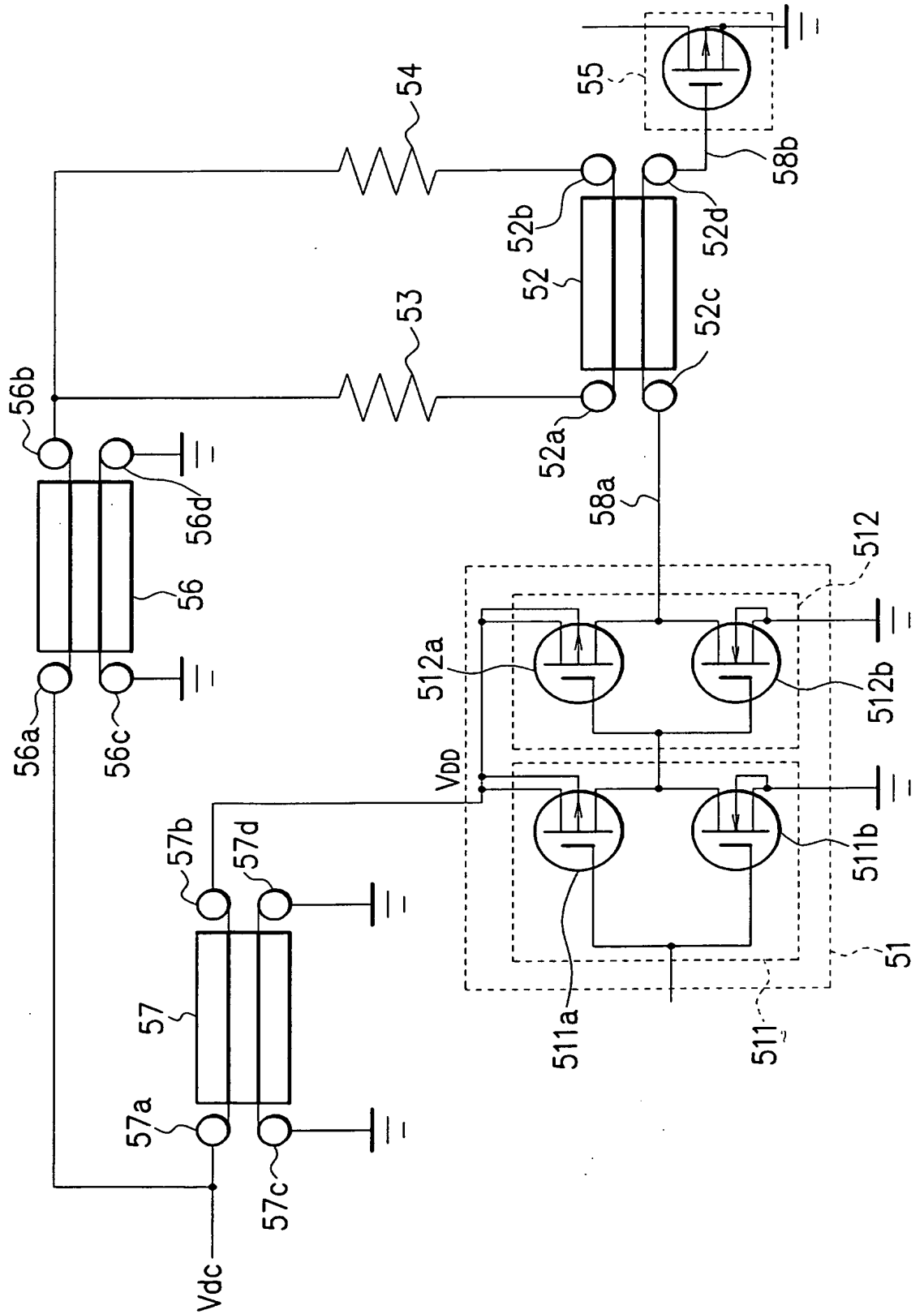


FIG. 40

